

Model Name: GA-H81M-H

Revision 1.1

SHEET

TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1150-A
05	CPU_LGA1150-B
06	CPU_LGA1150-C
07	DDR III CHANNEL A
08	DDR III CHANNEL B
09	PCH_FDI,DMI,USB,PCIE,NVRAM
10	PCH_DP,CLK BUFFER
11	PCH_HOST,SATA,PCI
12	PCH_GPIO,CTRL,AUDIO
13	PCH_PWR,GND
14	PCI EXPRESS*16 SLOT
15	PCI EXPRESS X1 *2 SLOT
16	ITE 8620
17	COM,KB_MS_USB,USB30_20
18	HWM,FAN CTRL,OV,-PROCHOT
19	DUAL BIOS
20	FP,FUSB,SPK,SATALED
21	Realtek ALC887-VD2
22	REAR AUDIO JACK
23	REALTEK RTL8111F
24	DISCRETE POWER
25	ATX , CLOCK GEN
26	VCORE ISL95812_1
27	VCORE ISL95812_2

SHEET

TITLE

28	RT8120_DDR POWER
29	HDMI
30	
31	
32	

**Gigabyte Technology**

Cover Sheet

Size Custom	Document Number <b>GA-H81M-H</b>	Rev <b>1.1</b>
Date: Thursday, October 31, 2013	Sheet 1 of 29	

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## 1

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BLOCK DIAGRAM

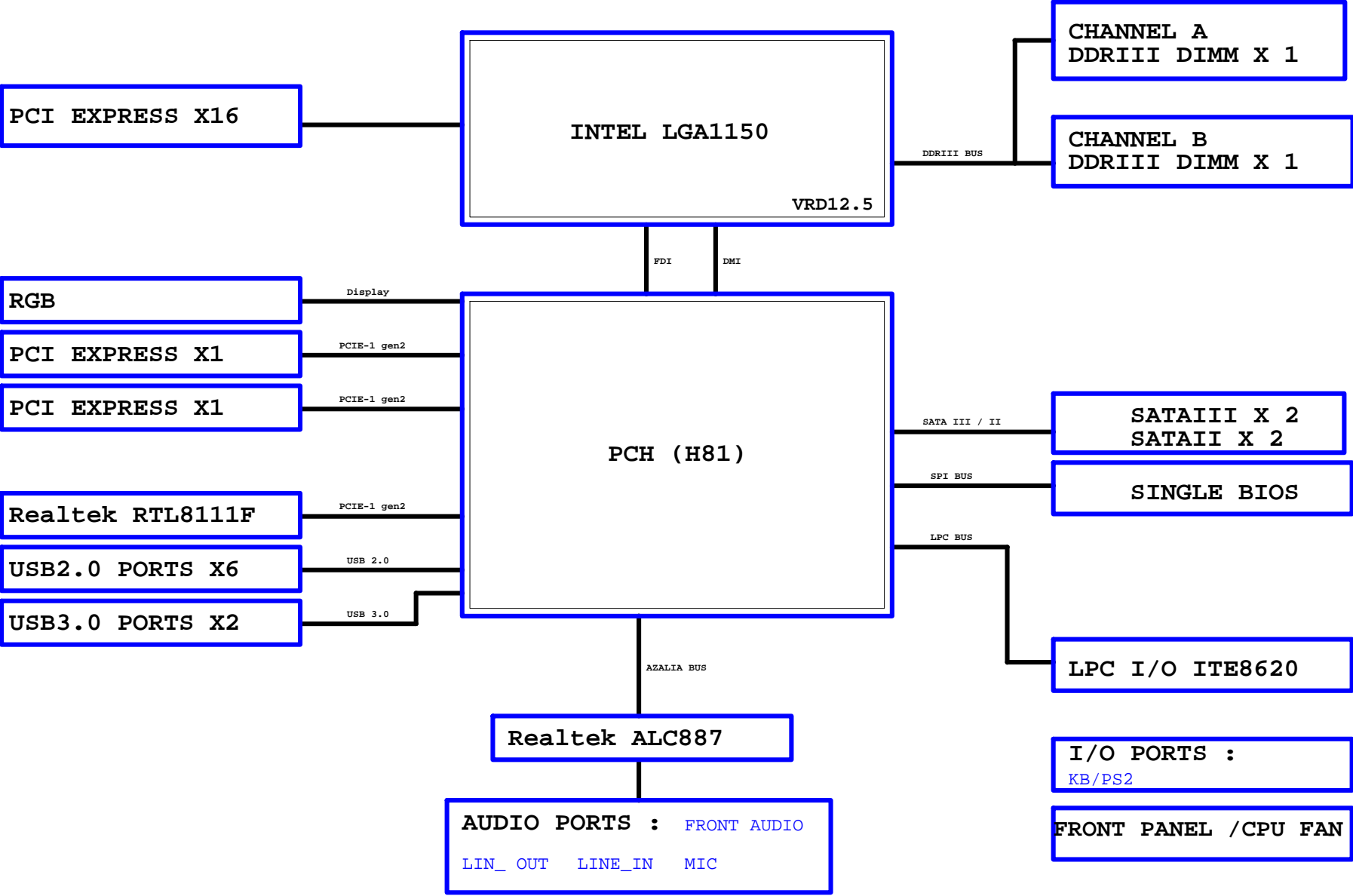


Figure 10-10: Pinmux for the Haswell PCH (continued)

Diagram illustrating the pin connections for the LGA1150D package, showing connections to various signals and components.

**Left Side Connections:**

- (9) FDI\_CSYNC → FDI\_CSYNC D16
- (9) FDI\_INT → FDI\_INT D18
- VCCIOA\_L → WR23 → 24.9/4/1 → FDI\_RCOMP R4
- (10) N\_DP\_CLK → U5
- (10) N\_DP\_CLK → U6
- ✗ E16 → EDP\_DISP\_UTIL
- ✗ K11 → RSVD\_TP
- ✗ J12 → RSVD\_TP
- FDI\_TXN0 B14
- FDI\_TXP0 A14
- FDI\_TXN1 C13
- FDI\_TXP1 B13

**Right Side Connections:**

- DDI1\_TXP0 E17 ✗
- DDI1\_TXN0 E17 ✗
- DDI1\_TXP1 F18 ✗
- DDI1\_TXN1 G18 ✗
- DDI1\_TXP2 G19 ✗
- DDI1\_TXN2 H19 ✗
- DDI1\_TXP3 F20 ✗
- DDI1\_TXN3 G20 ✗
- SSC\_DPCLKN D19 → HDMI\_TX2 (29)
- SSC\_DPCLKP E19 → HDMI\_TX2- (29)
- DDI2\_TXN0 C20 → HDMI\_TX1 (29)
- DDI2\_TXP1 D20 → HDMI\_TX1- (29)
- DDI2\_TXP2 D21 → HDMI\_TX0 (29)
- DDI2\_TXN2 E21 → HDMI\_TX0- (29)
- DDI2\_TXP3 C22 → HDMI\_TXC (29)
- DDI2\_TXN3 D22 → HDMI\_TXC- (29)
- DDI3\_TXP0 B15 ✗
- DDI3\_TXN0 C15 ✗
- DDI3\_TXP1 A16 ✗
- DDI3\_TXN1 B16 ✗
- DDI3\_TXP2 B17 ✗
- DDI3\_TXN2 C17 ✗
- DDI3\_TXP3 A18 ✗
- DDI3\_TXN3 B18 ✗

**Bottom Connections:**

- FDI\_EDP\_TXN0 DDI3\_TXP0
- FDI\_EDP\_TXP0 DDI3\_TXN0
- FDI\_EDP\_TXN1 DDI3\_TXP1
- FDI\_EDP\_TXP1 DDI3\_TXN1

**Legend:**

- ✗: Not connected
- : Connected

**Component Labels:**

- FDI\_CSYNC
- FDI\_INT
- DP\_RCOMP
- SSC\_DPCLKN
- SSC\_DPCLKP
- EDP\_DISP\_UTIL
- RSVD\_TP
- FDI\_EDP\_TXN0
- FDI\_EDP\_TXP0
- FDI\_EDP\_TXN1
- FDI\_EDP\_TXP1

**Package and Board Information:**

- LGA1150D
- HASWELL/[10SC1-F01150-11R, 10SC1-F01150-12R]

**Notes:**

- FDI: 12/4/5/4/12 (breakout min 6/4/4/4/6)
- Impedance = 85 ± 17.5%

**Legend:**

- FDI\_TXP0\_01 → FDI\_TXP0[0..1] (9)
- FDI\_TXN0\_01 → FDI\_TXN0[0..1] (9)

PCIEX16:16/5/5/16(breakout min 10/4/4/4/10)									
Impedance=80 +- 1.5%									
LGA1150C									
PA EXP RXP0	E15	PEG_RXP0	PEG_TXP0	A12	PA EXP TXP0				
PA EXP RXN0	F15	PEG_RXN0	PEG_TXN0	B12	PA EXP TXN0				
PA EXP RXP1	D14	PEG_RXP1	PEG_TXP1	B11	PA EXP TXP1				
PA EXP RXN1	E14	PEG_RXN1	PEG_TXN1	C11	PA EXP TXN1				
PA EXP RXP2	E13	PEG_RXP2	PEG_TXP2	C10	PA EXP TXP2				
PA EXP RXN2	F13	PEG_RXN2	PEG_TXN2	D10	PA EXP TXN2				
PA EXP RXP3	D12	PEG_RXP3	PEG_TXP3	B9	PA EXP TXP3				
PA EXP RXN3	E12	PEG_RXN3	PEG_TXN3	C9	PA EXP TXN3				
PA EXP RXP4	E11	PEG_RXP4	PEG_TXP4	C8	PA EXP TXP4				
PA EXP RXN4	F11	PEG_RXN4	PEG_TXN4	D8	PA EXP TXN4				
PA EXP RXP5	F10	PEG_RXP5	PEG_TXP5	B7	PA EXP TXP5				
PA EXP RXN5	G10	PEG_RXN5	PEG_TXN5	C7	PA EXP TXN5				
PA EXP RXP6	F9	PEG_RXP6	PEG_TXP6	A6	PA EXP TXP6				
PA EXP RXN6	F9	PEG_RXN6	PEG_TXN6	B6	PA EXP TXN6				
PA EXP RXP7	F8	PEG_RXP7	PEG_TXP7	B5	PA EXP TXP7				
PA EXP RXN7	G8	PEG_RXN7	PEG_TXN7	C5	PA EXP TXN7				
PA EXP RXP8	D3	PEG_RXP8	PEG_TXP8	E1	PA EXP TXP8				
PA EXP RXN8	D4	PEG_RXN8	PEG_TXN8	F2	PA EXP TXN8				
PA EXP RXP9	E4	PEG_RXP9	PEG_TXP9	F1	PA EXP TXP9				
PA EXP RXN9	E5	PEG_RXN9	PEG_TXN9	F3	PA EXP TXN9				
PA EXP RXP10	F5	PEG_RXP10	PEG_TXP10	G1	PA EXP TXP10				
PA EXP RXN10	F6	PEG_RXN10	PEG_TXN10	G2	PA EXP TXN10				
PA EXP RXP11	G4	PEG_RXP11	PEG_TXP11	H2	PA EXP TXP11				
PA EXP RXN11	G5	PEG_RXN11	PEG_TXN11	H3	PA EXP TXN11				
PA EXP RXP12	H5	PEG_RXP12	PEG_TXP12	J1	PA EXP TXP12				
PA EXP RXN12	H6	PEG_RXN12	PEG_TXN12	J2	PA EXP TXN12				
PA EXP RXP13	J4	PEG_RXP13	PEG_TXP13	K2	PA EXP TXP13				
PA EXP RXN13	J5	PEG_RXN13	PEG_TXN13	K3	PA EXP TXN13				
PA EXP RXP14	K5	PEG_RXP14	PEG_TXP14	M2	PA EXP TXP14				
PA EXP RXN14	K6	PEG_RXN14	PEG_TXN14	M3	PA EXP TXN14				
PA EXP RXP15	L4	PEG_RXP15	PEG_TXP15	L1	PA EXP TXP15				
PA EXP RXN15	L5	PEG_RXN15	PEG_TXN15	L2	PA EXP TXN15				
A DMI_0RXP	U3	DMI_RXP0	DMI_TXP0	A44	A DMI_0TXP				
A DMI_0RXN	U3	DMI_RXN0	DMI_TXN0	A45	A DMI_0TXN				
A DMI_1RXP	T1	DMI_RXP1	DMI_TXP1	A46	A DMI_1TXP				
A DMI_1RXN	V1	DMI_RXN1	DMI_TXN1	A47	A DMI_1TXN				
A DMI_2RXP	W2	DMI_RXP2	DMI_TXP2	A48	A DMI_2TXP				
A DMI_2RXN	Y2	DMI_RXN2	DMI_TXN2	A49	A DMI_2TXN				
A DMI_3RXP	W3	DMI_RXP3	DMI_TXP3	A50	A DMI_3TXP				
A DMI_3RXN	W3	DMI_RXN3	DMI_TXN3	A51	A DMI_3TXN				
	X1	RSVD_TP							
	C2	RSVD_TP							
	B3	RSVD_TP							
	A4	RSVD_TP							
		RSVD_TP							
W=12 mil out of CPU									
S=15 mil out of CPU									
VCIOA_L0	WR15	24.9/4.1	GRCOMP	P3	RSCM_COMP				
HASWELL/10SC1-F01150-11R 10SC1-F01150-12R									

**1.1V分壓**

VCC3

WR26  
2K4/1/X

WR31  
1K4/1/X

A\_CPUREST

BC102  
1n4/07R/50V/K

A\_CPUREST (11,16)

**For IT8620 Ctrl**

The diagram illustrates the timing relationships for CPU Power Up/Down (PU/PD) signals. The signals are categorized into three groups: CPU\_VTT\_OR, CPU\_PU/PD, and CPU\_PWR/DBG. The signals are connected to various pins and internal components, with their states and transitions indicated by the diagram.

**CPU\_VTT\_OR Signals:**

- WR3: 80.9/4/1 (PVIDSOUT)
- WR2: 115/4/1 (PVIDSOUT)
- WR4: 75/4/1 (PVIDALRT)
- WR14: 51/4/1/X (A TMS)
- WR16: 51/4/1/X (A TDO)
- WR17: 51/4/1/X (A TDI)
- WR30: 51/4/1 (A -HPRDY)
- WR11: 51/4/1 (A TCK)
- WR9: 51/4/1 (A -TRST)
- WR29: 1K4/1/X (A PECL)
- WR10: 1K4/1/X (A CATERR)
- WR25: 1K4/1 (A -PROCHOT)
- WR56: 51/4/1/X (N CPUPWROK)
- WR55: 1K4/1/X

**CPU\_PU/PD Signals:**

- A -THRMTTRIP: WR8: 1K4/1 (VCC1\_05\_PCH)
- A PWR\_DEBUG: WR34: 150/4/1 (VCC1\_05\_PCH)
- WR33: 10K4/1/X
- WR21: 8.2K/4/X (3VDUAL)
- A -DBR: WR20: 0/4/X (N\_N\_SYS\_RST)

**CPU\_PWR/DBG Signals:**

- A DDR\_COMP0: WR28: 100/4/1
- A DDR\_COMP1: WR19: 75/4/1
- A DDR\_COMP2: WR22: 100/4/1
- A TESTLOW\_1: WR18: 49.9/4/1
- A TESTLOW\_2: WR12: 49.9/4/1
- A HSW\_CFG\_RCOMP: WR24: 49.9/4/1

DDR\_15V

WR62  
100k/4%

WR60  
100k/4%

A SM VREF

WC3  
0.1uF/4X7R/16V/K

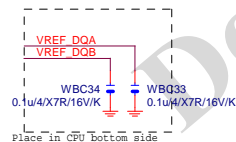
## LGA1150 (A)

LGA1150A		DDR0_MA0	DDR0_D00	AD38	MDA0
MAAA0	AU13	DDR0_MA1	DDR0_D01	AD39	MDA1
MAAA1	AV16	DDR0_MA2	DDR0_D02	AF38	MDA2
MAAA2	AU16	DDR0_MA3	DDR0_D03	AF39	MDA3
MAAA3	AW17	DDR0_MA4	DDR0_D04	AD37	MDA4
MAAA4	AU18	DDR0_MA5	DDR0_D05	AD40	MDA5
MAAA5	AW18	DDR0_MA6	DDR0_D06	AE37	MDA6
MAAA6	AV17	DDR0_MA7	DDR0_D07	AF40	MDA7
MAAA7	AT18	DDR0_MA8	DDR0_D08	AH40	MDA9
MAAA8	AU18	DDR0_MA9	DDR0_D09	AH39	MDA10
MAAA9	AT19	DDR0_MA10	DDR0_D10	AK38	MDA11
MAAA10	AW11	DDR0_MA11	DDR0_D11	AK39	MDA12
MAAA11	AV19	DDR0_MA12	DDR0_D12	AH37	MDA12
MAAA12	AU19	DDR0_MA13	DDR0_D13	AH38	MDA14
MAAA13	AT20	DDR0_MA14	DDR0_D14	AK37	MDA14
MAAA14	AW21	DDR0_MA15	DDR0_D15	AK40	MDA15
MAAA15	AU21	DDR0_MA16	DDR0_D16	AM40	MDA17
MODT_A0	AW10	DDR0_ODT0	DDR0_D17	AM39	MDA21
MODT_A1	AY8	DDR0_ODT1	DDR0_D18	AP38	MDA18
AW9		DDR0_ODT2	DDR0_D19	AP39	MDA19
AW8		DDR0_ODT3	DDR0_D20	AM37	MDA20
AW33		DDR0_ECC0	DDR0_D21	AM38	MDA16
AW33		DDR0_ECC1	DDR0_D22	AP37	MDA22
AU31		DDR0_ECC2	DDR0_D23	AP40	MDA23
AW31		DDR0_ECC3	DDR0_D24	AW37	MDA29
AU33		DDR0_ECC4	DDR0_D25	AU35	MDA26
AT33		DDR0_ECC5	DDR0_D26	AU35	MDA27
AT31		DDR0_ECC6	DDR0_D27	T37	MDA28
AW31		DDR0_ECC7	DDR0_D28	AU37	MDA24
SBAA0	SBAA0	DDR0_BA0	DDR0_D29	AT35	MDA30
SBAA1	SBAA1	DDR0_BA1	DDR0_D30	AW35	MDA31
SBAA2	SBAA2	DDR0_BA2	DDR0_D31	AY6	MDA33
CKEA0	CKEA0	DDR0_CK0	DDR0_D32	AU6	MDA37
CKEA1	CKEA1	DDR0_CK1	DDR0_D33	AW6	MDA36
CSA0	CSA0	DDR0_CS_N0	DDR0_D34	AW4	MDA38
CSA1	CSA1	DDR0_CS_N1	DDR0_D35	AW4	MDA39
DCLKA0	DCLKA0	DDR0_CLK_P0	DDR0_D36	AR1	MDA41
DCLKA1	DCLKA1	DDR0_CLK_P1	DDR0_D37	AR4	MDA45
DCLKA2	DCLKA2	DDR0_CLK_P2	DDR0_D38	AN3	MDA42
DCLKA3	DCLKA3	DDR0_CLK_P3	DDR0_D39	AN4	MDA43
DCLKA4	DCLKA4	DDR0_CLK_P4	DDR0_D40	AR2	MDA44
DCLKA5	DCLKA5	DDR0_CLK_P5	DDR0_D41	AR3	MDA40
DCLKA6	DCLKA6	DDR0_CLK_P6	DDR0_D42	AN2	MDA46
DCLKA7	DCLKA7	DDR0_CLK_P7	DDR0_D43	AN1	MDA47
DCLKA8	DCLKA8	DDR0_CLK_P8	DDR0_D44	AL1	MDA49
DCLKA9	DCLKA9	DDR0_CLK_P9	DDR0_D45	AL4	MDA53
DCLKA10	DCLKA10	DDR0_CLK_P10	DDR0_D46	AJ4	MDA51
DCLKA11	DCLKA11	DDR0_CLK_P11	DDR0_D47	AL2	MDA52
DCLKA12	DCLKA12	DDR0_CLK_P12	DDR0_D48	AJ2	MDA54
DCLKA13	DCLKA13	DDR0_CLK_P13	DDR0_D49	AJ1	MDA55
DCLKA14	DCLKA14	DDR0_CLK_P14	DDR0_D50	AG1	MDA57
DCLKA15	DCLKA15	DDR0_CLK_P15	DDR0_D51	AG4	MDA61
DCLKA16	DCLKA16	DDR0_CLK_P16	DDR0_D52	AE3	MDA58
DCLKA17	DCLKA17	DDR0_CLK_P17	DDR0_D53	E4	MDA59
DCLKA18	DCLKA18	DDR0_CLK_P18	DDR0_D54	AG2	MDA60
DCLKA19	DCLKA19	DDR0_CLK_P19	DDR0_D55	AG3	MDA56
DCLKA20	DCLKA20	DDR0_CLK_P20	DDR0_D56	AE2	MDA62
DCLKA21	DCLKA21	DDR0_CLK_P21	DDR0_D57	AE1	MDA63
DCLKA22	DCLKA22	DDR0_CLK_P22	DDR0_D58	AE39	DQSA0
DCLKA23	DCLKA23	DDR0_CLK_P23	DDR0_D59	AJ39	DQSA1
DCLKA24	DCLKA24	DDR0_CLK_P24	DDR0_D60	AN39	DQSA2
DCLKA25	DCLKA25	DDR0_CLK_P25	DDR0_D61	AV36	DQSA3
DCLKA26	DCLKA26	DDR0_CLK_P26	DDR0_D62	AV5	DQSA4
DCLKA27	DCLKA27	DDR0_CLK_P27	DDR0_D63	AP3	DQSA5
DCLKA28	DCLKA28	DDR0_CLK_P28	DDR0_D64	AK3	DQSA6
DCLKA29	DCLKA29	DDR0_CLK_P29	DDR0_D65	AF3	DQSA7
DCLKA30	DCLKA30	DDR0_CLK_P30	DDR0_D66	AV32	DQSA0
DCLKA31	DCLKA31	DDR0_CLK_P31	DDR0_D67	AE38	DQSA1
DCLKA32	DCLKA32	DDR0_CLK_P32	DDR0_D68	AJ38	DQSA2
DCLKA33	DCLKA33	DDR0_CLK_P33	DDR0_D69	AN38	DQSA3
DCLKA34	DCLKA34	DDR0_CLK_P34	DDR0_D70	AJ36	DQSA4
DCLKA35	DCLKA35	DDR0_CLK_P35	DDR0_D71	AW5	DQSA5
DCLKA36	DCLKA36	DDR0_CLK_P36	DDR0_D72	AP2	DQSA6
DCLKA37	DCLKA37	DDR0_CLK_P37	DDR0_D73	AK2	DQSA7
DCLKA38	DCLKA38	DDR0_CLK_P38	DDR0_D74	AF2	DQSA7
DCLKA39	DCLKA39	DDR0_CLK_P39	DDR0_D75	AU32	DQSA7

HASWELL[10SC1-F01150-11R\_10SC1-F01150-12R]

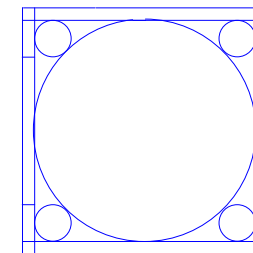
## LGA1150 (B)

LGA1150B		DDR1_MA0	DDR1_D00	AE34	MDB0
MAAB0	AL19	DDR1_MA1	DDR1_D01	AE35	MDB1
MAAB1	AK23	DDR1_MA2	DDR1_D02	AG35	MDB2
MAAB2	AM22	DDR1_MA3	DDR1_D03	AH35	MDB3
MAAB3	AM23	DDR1_MA4	DDR1_D04	AD34	MDB4
MAAB4	AP23	DDR1_MA5	DDR1_D05	AD35	MDB5
MAAB5	AL23	DDR1_MA6	DDR1_D06	AG34	MDB6
MAAB6	AY24	DDR1_MA7	DDR1_D07	AH34	MDB7
MAAB7	AY25	DDR1_MA8	DDR1_D08	AL34	MDB8
MAAB8	AU26	DDR1_MA9	DDR1_D09	AL35	MDB9
MAAB9	AW25	DDR1_MA10	DDR1_D10	AK31	MDB10
MAAB10	AP18	DDR1_MA11	DDR1_D11	AL31	MDB11
MAAB11	AY25	DDR1_MA12	DDR1_D12	AK34	MDB12
MAAB12	AY26	DDR1_MA13	DDR1_D13	AK35	MDB13
MAAB13	AR15	DDR1_MA14	DDR1_D14	AK32	MDB14
MAAB14	AV27	DDR1_MA15	DDR1_D15	AL32	MDB15
MAAB15	AY28	DDR1_MA16	DDR1_D16	AL34	MDB17
MODT_B0	AM17	DDR1_ODT0	DDR1_D17	AP34	MDB21
MODT_B1	AL16	DDR1_ODT1	DDR1_D18	AK31	MDB19
AW16		DDR1_ODT2	DDR1_D19	AP31	MDB23
AK15		DDR1_ODT3	DDR1_D20	AP35	MDB20
AK15		DDR1_ODT4	DDR1_D21	AP35	MDB16
AK15		DDR1_ODT5	DDR1_D22	AK32	MDB18
AK15		DDR1_ODT6	DDR1_D23	AP32	MDB22
AK15		DDR1_ODT7	DDR1_D24	AM29	MDB25
AK15		DDR1_ODT8	DDR1_D25	AM28	MDB28
AK15		DDR1_ODT9	DDR1_D26	AR29	MDB27
AK15		DDR1_ODT10	DDR1_D27	AR28	MDB30
AK15		DDR1_ODT11	DDR1_D28	AL28	MDB29
AK15		DDR1_ODT12	DDR1_D29	AP29	MDB26
AK15		DDR1_ODT13	DDR1_D30	AP28	MDB31
AK15		DDR1_ODT14	DDR1_D31	AR12	MDB32
AK15		DDR1_ODT15	DDR1_D32	AP12	MDB33
AK15		DDR1_ODT16	DDR1_D33	AL13	MDB34
AK15		DDR1_ODT17	DDR1_D34	AL12	MDB35
AK15		DDR1_ODT18	DDR1_D35	AR13	MDB36
AK15		DDR1_ODT19	DDR1_D36	AP13	MDB37
AK15		DDR1_ODT20	DDR1_D37	AM13	MDB38
AK15		DDR1_ODT21	DDR1_D38	AM12	MDB39
AK15		DDR1_ODT22	DDR1_D39	AR9	MDB45
AK15		DDR1_ODT23	DDR1_D40	AP9	MDB41
AK15		DDR1_ODT24	DDR1_D41	AR6	MDB47
AK15		DDR1_ODT25	DDR1_D42	AP6	MDB43
AK15		DDR1_ODT26	DDR1_D43	AR10	MDB44
AK15		DDR1_ODT27	DDR1_D44	AP10	MDB40
AK15		DDR1_ODT28	DDR1_D45	AR7	MDB46
AK15		DDR1_ODT29	DDR1_D46	AP7	MDB42
AK15		DDR1_ODT30	DDR1_D47	AM9	MDB52
AK15		DDR1_ODT31	DDR1_D48	AL9	MDB53
AK15		DDR1_ODT32	DDR1_D49	AL6	MDB50
AK15		DDR1_ODT33	DDR1_D50	AL7	MDB55
AK15		DDR1_ODT34	DDR1_D51	AM10	MDB48
AK15		DDR1_ODT35	DDR1_D52	AL10	MDB49
AK15		DDR1_ODT36	DDR1_D53	AM6	MDB54
AK15		DDR1_ODT37	DDR1_D54	AM2	MDB51
AK15		DDR1_ODT38	DDR1_D55	AH6	MDB61
AK15		DDR1_ODT39	DDR1_D56	AH7	MDB60
AK15		DDR1_ODT40	DDR1_D57	AE6	MDB59
AK15		DDR1_ODT41	DDR1_D58	AE7	MDB63
AK15		DDR1_ODT42	DDR1_D59	AJ6	MDB56
AK15		DDR1_ODT43	DDR1_D60	AJ7	MDB57
AK15		DDR1_ODT44	DDR1_D61	AG6	MDB58
AK15		DDR1_ODT45	DDR1_D62	AF7	MDB62
AK15		DDR1_ODT46	DDR1_D63	AF35	DQSB0
AK15		DDR1_ODT47	DDR1_D64	AL33	DQSB1
AK15		DDR1_ODT48	DDR1_D65	AN28	DQSB2
AK15		DDR1_ODT49	DDR1_D66	AN28	DQSB3
AK15		DDR1_ODT50	DDR1_D67	AN12	DQSB4
AK15		DDR1_ODT51	DDR1_D68	AP8	DQSB5
AK15		DDR1_ODT52	DDR1_D69	AL8	DQSB6
AK15		DDR1_ODT53	DDR1_D70	AG7	DQSB7
AK15		DDR1_ODT54	DDR1_D71	AN25	DQSB0
AK15		DDR1_ODT55	DDR1_D72	AK33	DQSB1
AK15		DDR1_ODT56	DDR1_D73	AN33	DQSB2
AK15		DDR1_ODT57	DDR1_D74	AN29	DQSB3
AK15		DDR1_ODT58	DDR1_D75	AN13	DQSB4
AK15		DDR1_ODT59	DDR1_D76	AR8	DQSB5
AK15		DDR1_ODT60	DDR1_D77	AM8	DQSB6
AK15		DDR1_ODT61	DDR1_D78	AG6	DQSB7
AK15		DDR1_ODT62	DDR1_D79	AN26	DQSB7



HASWELL[10SC1-F01150-11R\_10SC1-F01150-12R]

## LGA1150 (CR)

CR  
CPU RETENTION/X

LGA1150



ILM\_BP/1156/CSP/ILM\_BP/1156/CSP/[12KRC-0F0001-52R\_12KRC-0F0001-51R]

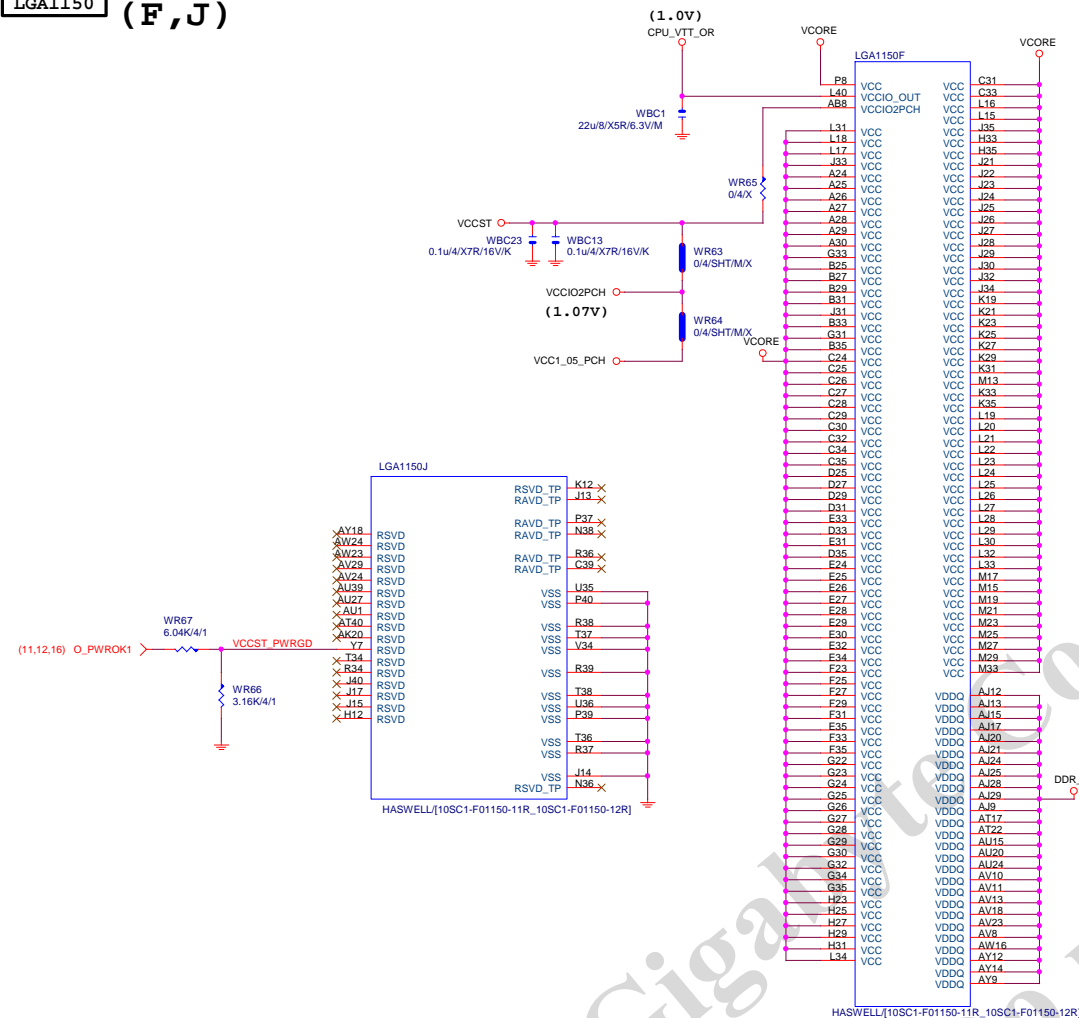
DDR BUS

(7)	MODT_A[0..1]	MODT_A10..11
(8)	MODT_B[0..1]	MODT_B10..11
(7)	MDA[0..63]	MDA10..63
(8)	MDB[0..63]	MDB10..63
(7)	DQSA[0..7]	DQSA10..71
(7)	DQSA[0..7]	DQSA10..71
(7)	MAAA[0..15]	MAAA10..15
(8)	MAAB[0..15]	MAAB10..15
(8)	DQSB[0..7]	DQSB10..71
(8)	DQSB[0..7]	DQSB10..71

Gigabyte Technology

Title		CPU LGA1150-B	
Size	Document Number	GA--H81M-H	
Custom			Rev 1.1
Date:	Thursday, October 31, 2013	Sheet	5 of 29

# LGA1150 (F,J)

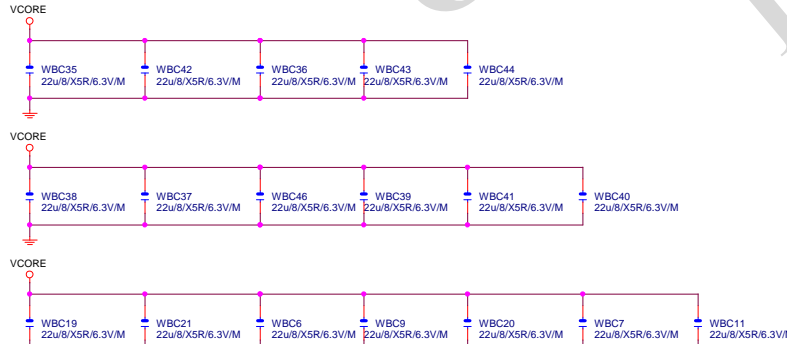


# LGA1155 (G,H,I)



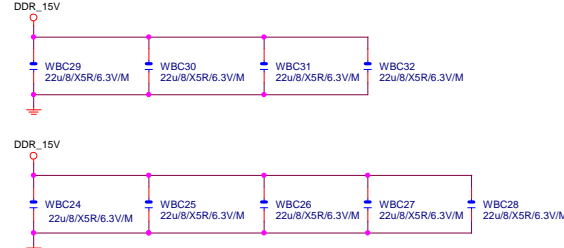
## VCore CAP

(X18)



## DDR CAP

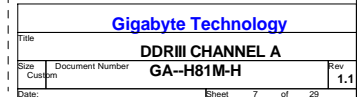
(X9)



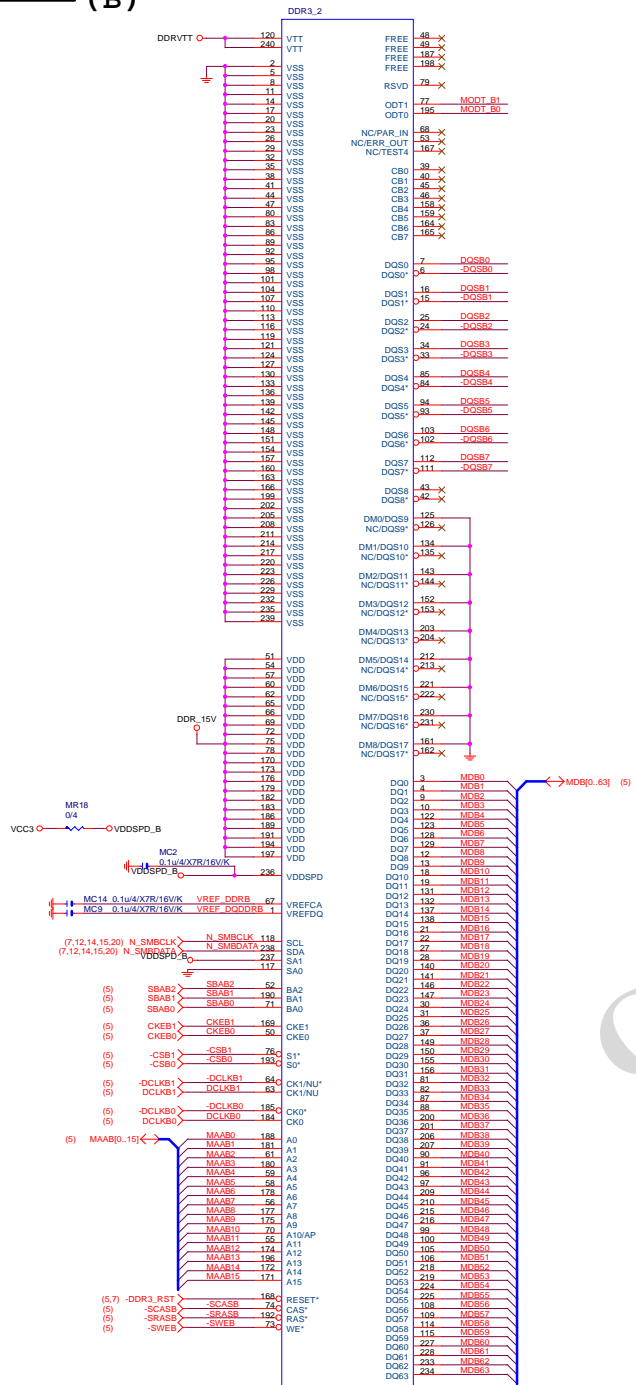
Gigabyte Technology

Title		CPU LGA1150-C	
Size	Document Number	GA-H81M-H	
Custpm			
Date:	Thursday, October 31, 2013	Sheet	6 of 29
			Rev 1.1

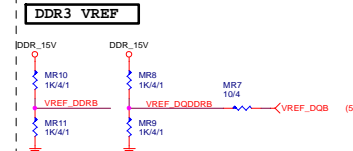
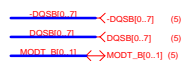
(A)



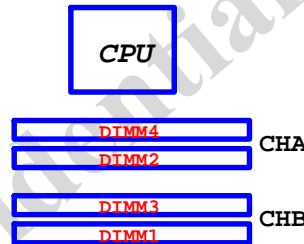
(B)



DDR3/240/BK/VA/D  
**BLACK CONNECTOR**



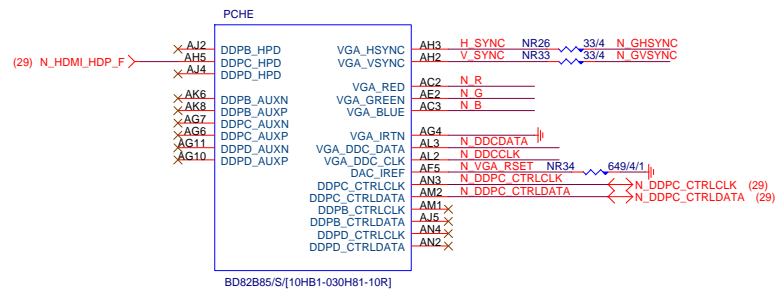
## COUPON



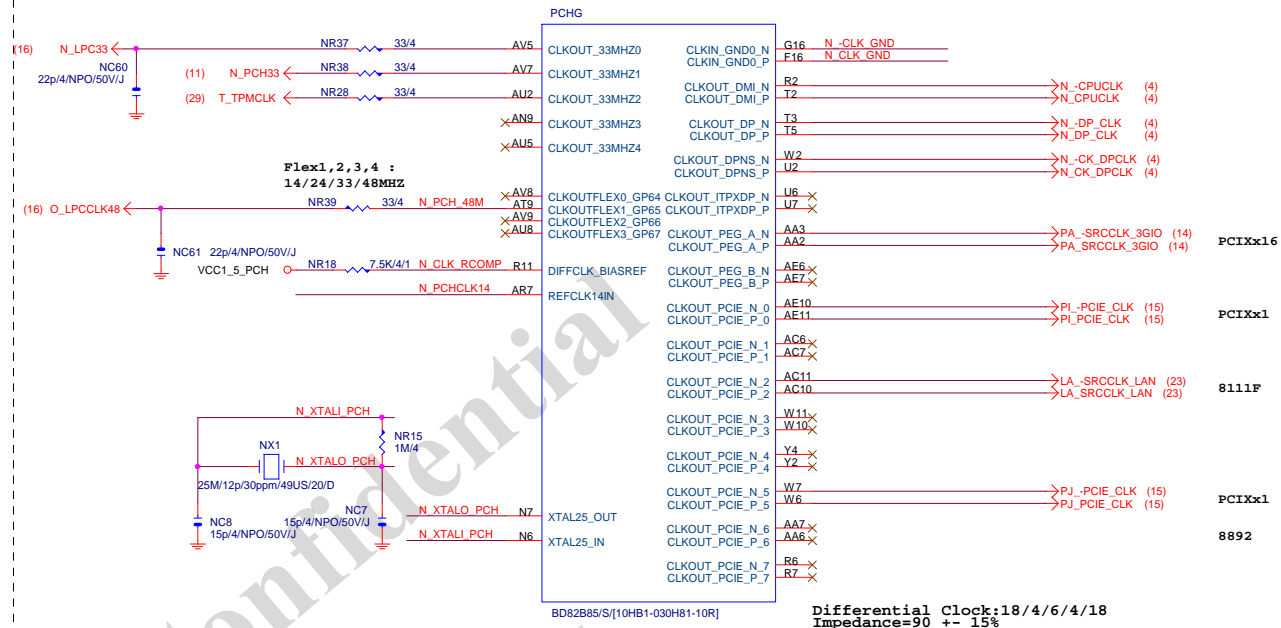




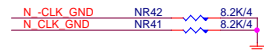
**PCH (E)**



**PCH (G)**



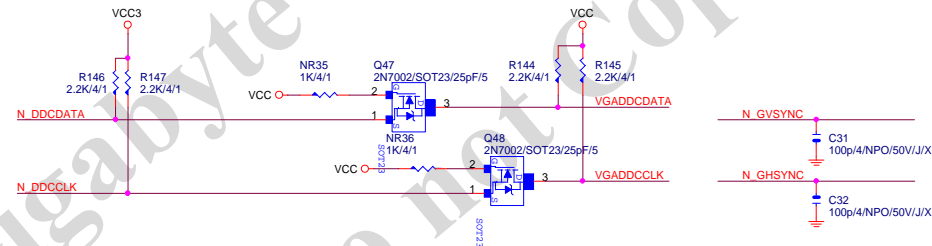
PCH CLK PD
------------



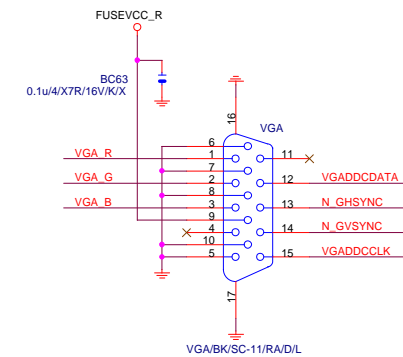
Mount for integrated clock Generation  
Mode



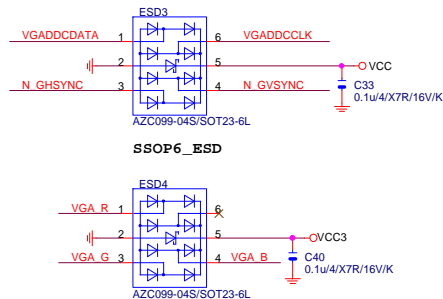
## VGA DDC



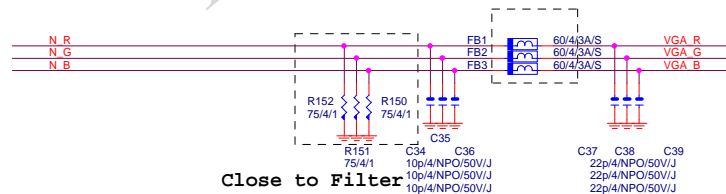
## VGA CONNECTOR



## VGA ESD



## VGA DDC



Close to Filter

Differential Clock:18/4/6/4/18  
Impedance=90 +- 15%

## Gigabyte Technology

### PCH DISPLAY\_CLK BUFFER

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## PCH (C)

SATA3 : 20/7.5/4.5/7.5/20 (breakout min 8/4/4/4/8)  
Impedance=90 +- 17.5%  
SATA2 : 15/7.5/4.5/7.5/15 (breakout min 8/4/4/4/8)  
Impedance=90 +- 17.5%

PCHC

CL\_CLK

CL\_DATA

CL\_RSTB

APWROK

PWM0

PWM1

PWM2

PWM3

TACH0\_GP17

TACH1\_GP1

TACH2\_GP6

TACH3\_GP7

TACH4\_GP8

TACH5\_GP69

SSTCTL

SCLOCK\_GP22

SLOAD\_GP38

SDATAOUT0\_GP39

SDATAOUT1\_GP48

GPIO

GPIO17

GPIO1

GPIO6

GPIO7

GPIO68

GPIO69

GPIO22

GPIO38

GPIO39

GPIO48

GPIO4

GPIO5

GPIO6

GPIO7

GPIO8

GPIO9

GPIO10

GPIO11

GPIO12

GPIO13

GPIO14

GPIO15

GPIO16

GPIO17

GPIO18

GPIO19

GPIO20

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GPIO256

GPIO257

GPIO258

GPIO259

GPIO260

GPIO261

GPIO262

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GPIO264

GPIO265

GPIO266

GPIO267

GPIO268

GPIO269

GPIO270

GPIO271

GPIO272

GPIO273

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GPIO280

GPIO281

GPIO282

GPIO283

GPIO284

GPIO285

GPIO286

GPIO287

GPIO288

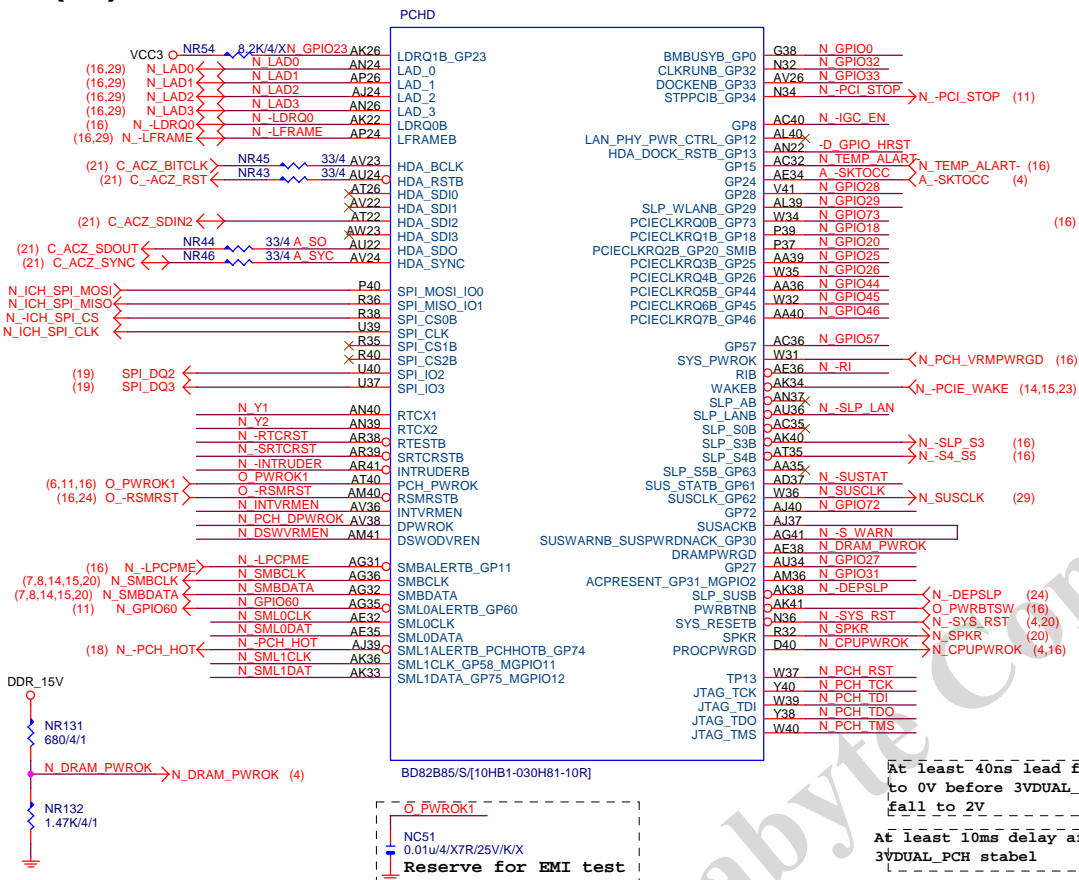
GPIO289

GPIO290

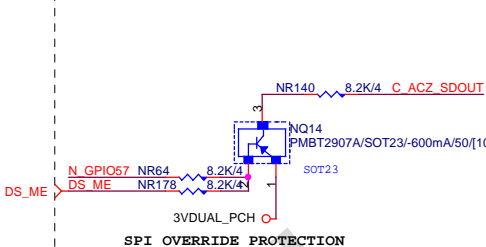
GPIO291

GPIO292

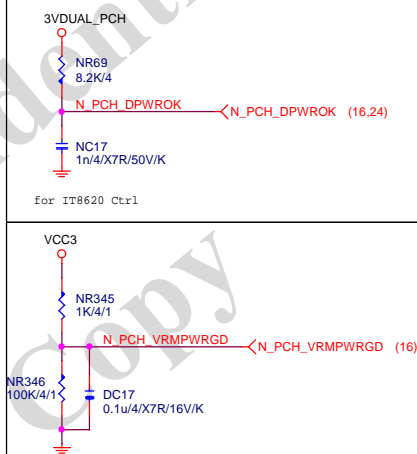
(D)



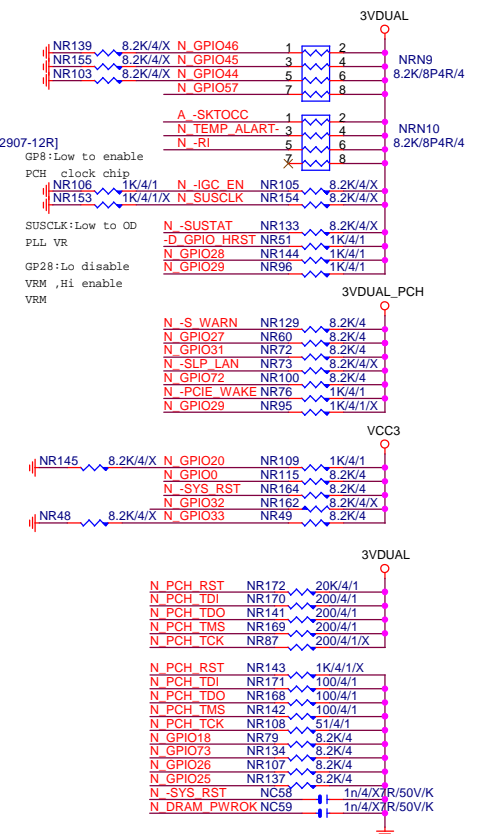
## ACZ\_SDOUT



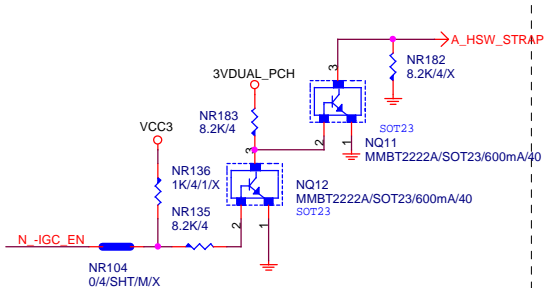
## PCH\_DPWROK



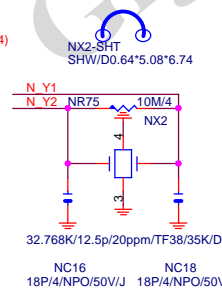
PCH	PU/PD
-----	-------



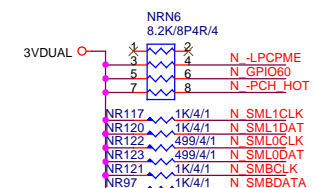
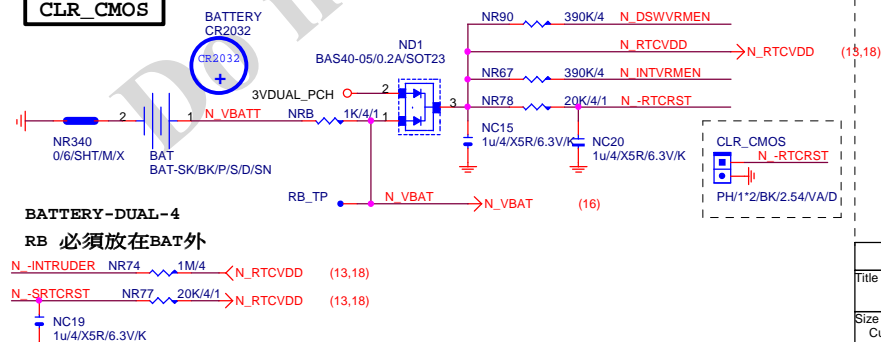
## HSW\_STRAP13



32.768KHZ



CLR_CMOS
----------

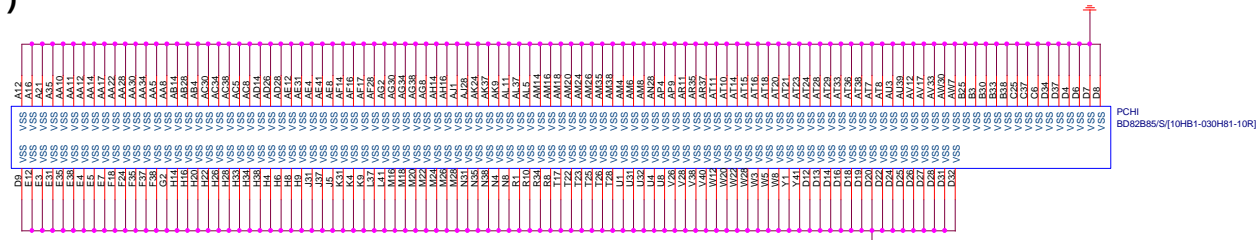


## Gigabyte Technology

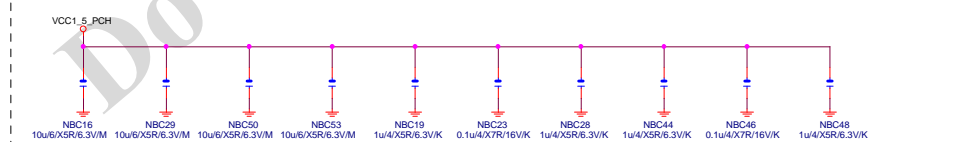
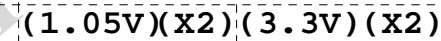
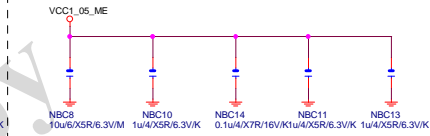
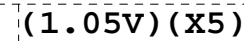
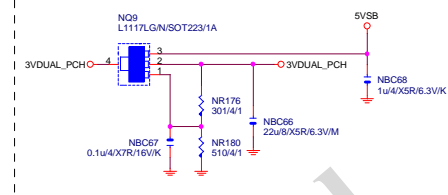
## PCH GPIO , CTRL , AUDIO

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**PCH (I)**

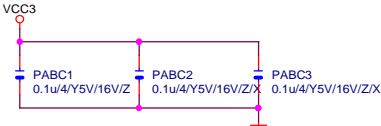


SHT PWR

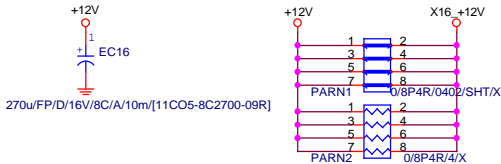




# PCIEX16 CAP



# PCIEX16 PROTECT SHT

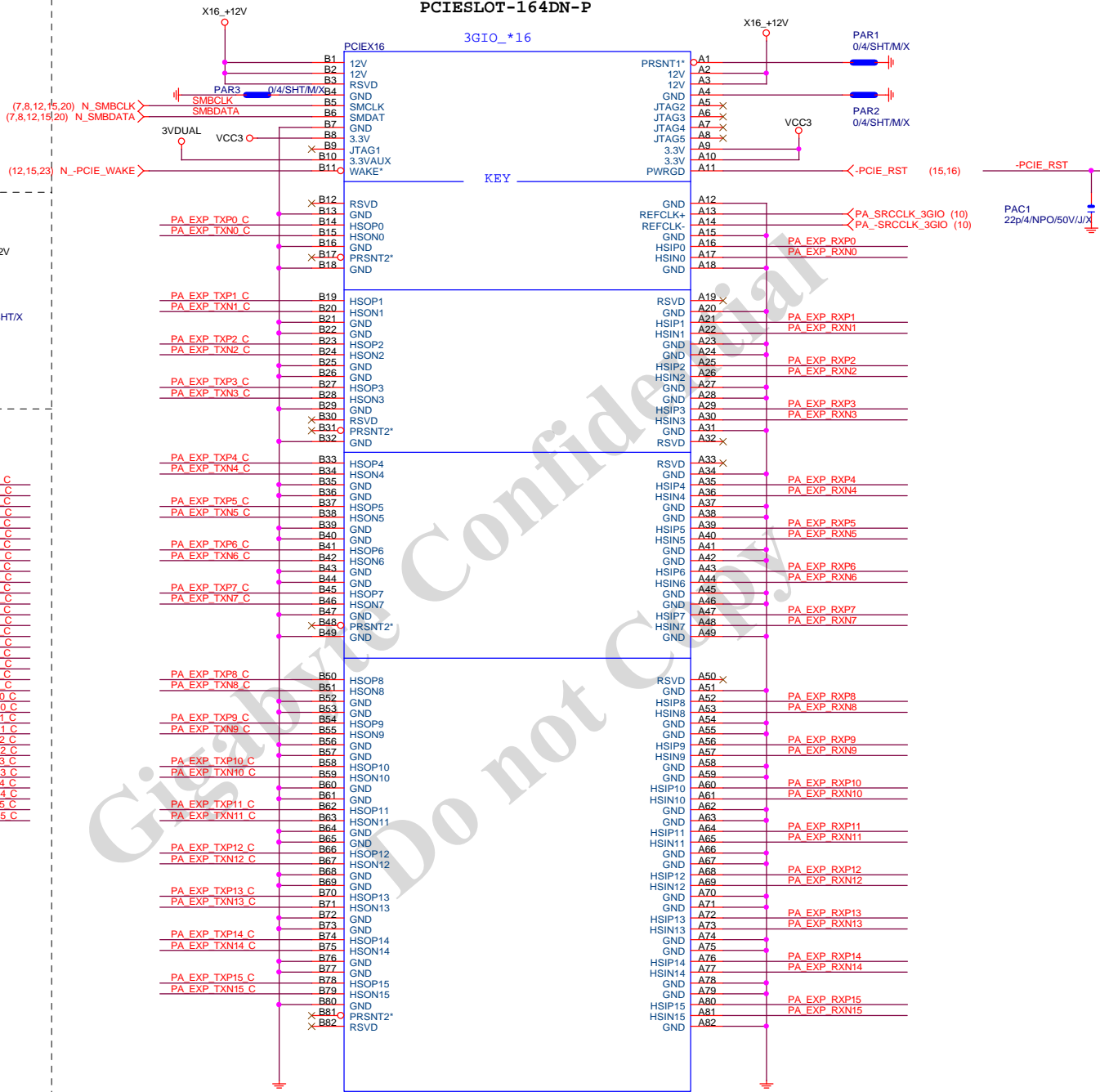


# PCIEX16 AC CAP

PA EXP TXP0	PAC5	0.22u4/X5R/6.3V/K	PA EXP TXP0 C
PA EXP TXN0	PAC4	0.22u4/X5R/6.3V/K	PA EXP TXN0 C
PA EXP TXP1	PAC6	0.22u4/X5R/6.3V/K	PA EXP TXP1 C
PA EXP TXN1	PAC7	0.22u4/X5R/6.3V/K	PA EXP TXN1 C
PA EXP TXP2	PAC8	0.22u4/X5R/6.3V/K	PA EXP TXP2 C
PA EXP TXN2	PAC9	0.22u4/X5R/6.3V/K	PA EXP TXN2 C
PA EXP TXP3	PAC10	0.22u4/X5R/6.3V/K	PA EXP TXP3 C
PA EXP TXN3	PAC11	0.22u4/X5R/6.3V/K	PA EXP TXN3 C
PA EXP TXP4	PAC12	0.22u4/X5R/6.3V/K	PA EXP TXP4 C
PA EXP TXN4	PAC13	0.22u4/X5R/6.3V/K	PA EXP TXN4 C
PA EXP TXP5	PAC14	0.22u4/X5R/6.3V/K	PA EXP TXP5 C
PA EXP TXN5	PAC15	0.22u4/X5R/6.3V/K	PA EXP TXN5 C
PA EXP TXP6	PAC16	0.22u4/X5R/6.3V/K	PA EXP TXP6 C
PA EXP TXN6	PAC17	0.22u4/X5R/6.3V/K	PA EXP TXN6 C
PA EXP TXP7	PAC19	0.22u4/X5R/6.3V/K	PA EXP TXP7 C
PA EXP TXN7	PAC18	0.22u4/X5R/6.3V/K	PA EXP TXN7 C
PA EXP TXP8	PAC20	0.22u4/X5R/6.3V/K	PA EXP TXP8 C
PA EXP TXN8	PAC21	0.22u4/X5R/6.3V/K	PA EXP TXN8 C
PA EXP TXP9	PAC22	0.22u4/X5R/6.3V/K	PA EXP TXP9 C
PA EXP TXN9	PAC23	0.22u4/X5R/6.3V/K	PA EXP TXN9 C
PA EXP TXP10	PAC24	0.22u4/X5R/6.3V/K	PA EXP TXP10 C
PA EXP TXN10	PAC25	0.22u4/X5R/6.3V/K	PA EXP TXN10 C
PA EXP TXP11	PAC26	0.22u4/X5R/6.3V/K	PA EXP TXP11 C
PA EXP TXN11	PAC27	0.22u4/X5R/6.3V/K	PA EXP TXN11 C
PA EXP TXP12	PAC28	0.22u4/X5R/6.3V/K	PA EXP TXP12 C
PA EXP TXN12	PAC29	0.22u4/X5R/6.3V/K	PA EXP TXN12 C
PA EXP TXP13	PAC30	0.22u4/X5R/6.3V/K	PA EXP TXP13 C
PA EXP TXN13	PAC31	0.22u4/X5R/6.3V/K	PA EXP TXN13 C
PA EXP TXP14	PAC32	0.22u4/X5R/6.3V/K	PA EXP TXP14 C
PA EXP TXN14	PAC33	0.22u4/X5R/6.3V/K	PA EXP TXN14 C
PA EXP TXP15	PAC34	0.22u4/X5R/6.3V/K	PA EXP TXP15 C
PA EXP TXN15	PAC35	0.22u4/X5R/6.3V/K	PA EXP TXN15 C

PA EXP RXIP0.15] >>>PA\_EXP\_RXP[0.15] (4)  
PA EXP RXN0.15] >>>PA\_EXP\_RXN[0.15] (4)  
PA EXP TXIP0.15] >>>PA\_EXP\_TXP[0.15] (4)  
PA EXP TXN0.15] >>>PA\_EXP\_TXN[0.15] (4)

# PCIEX16 SLOT

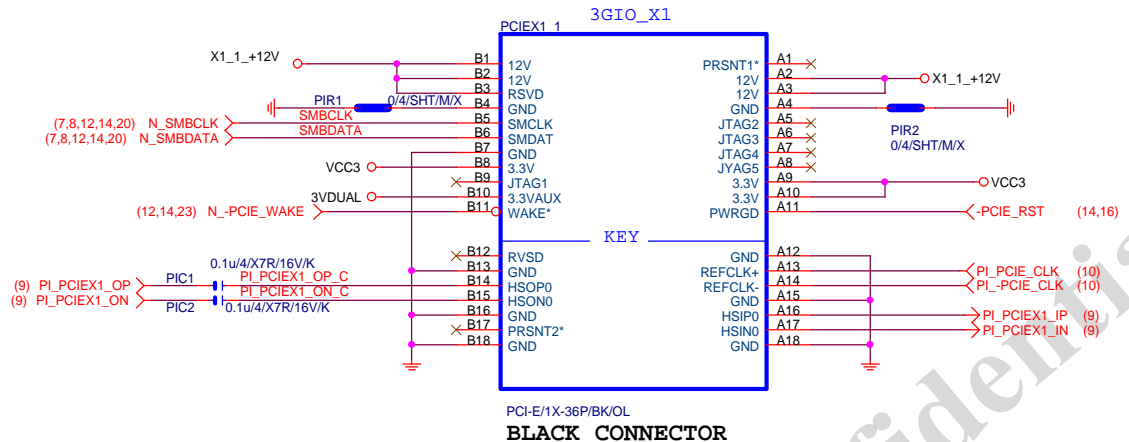
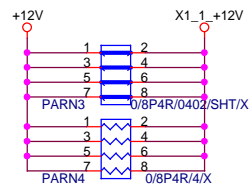


BLACK CONNECTOR

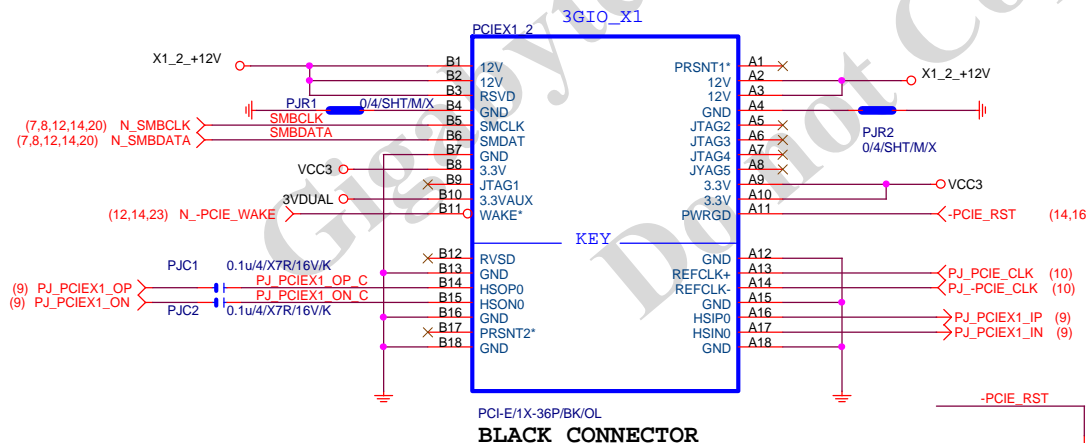
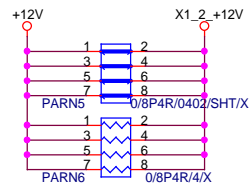
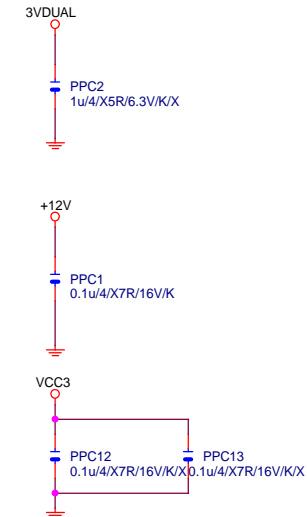
Gigabyte Technology		
Title		
PCI EXPRESS * 16		
Size	Document Number	Rev
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# PCIEX1 SLOT

# PCIEX1 PROTECT SHT



PCI-E/1X-36P/BK/OL  
BLACK CONNECTOR



PCI-E/1X-36P/BK/OL  
BLACK CONNECTOR



Gigabyte Technology			
PCI EXPRESS X 1 PORT			
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COM

KB/MS

KB\_MS ESD

USB2.0 PWR

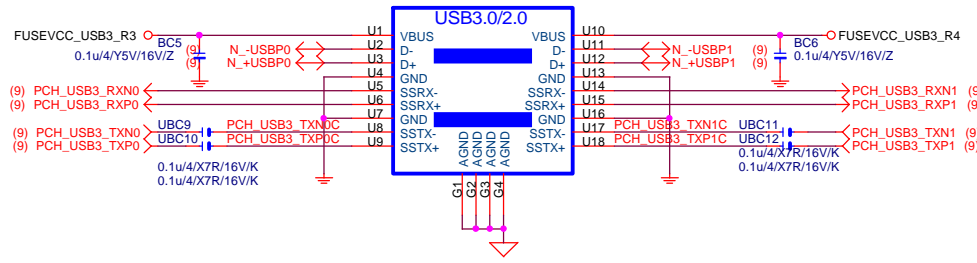
COM RI

USB30\_20

USB30\_20 ESD PROTECT

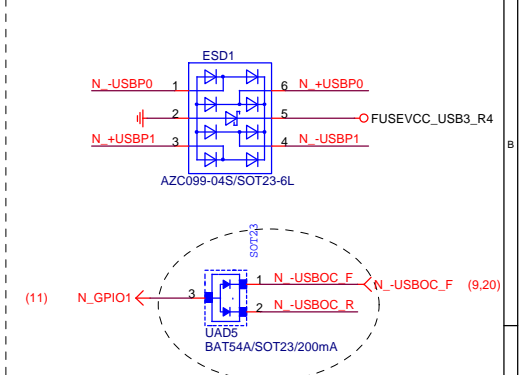
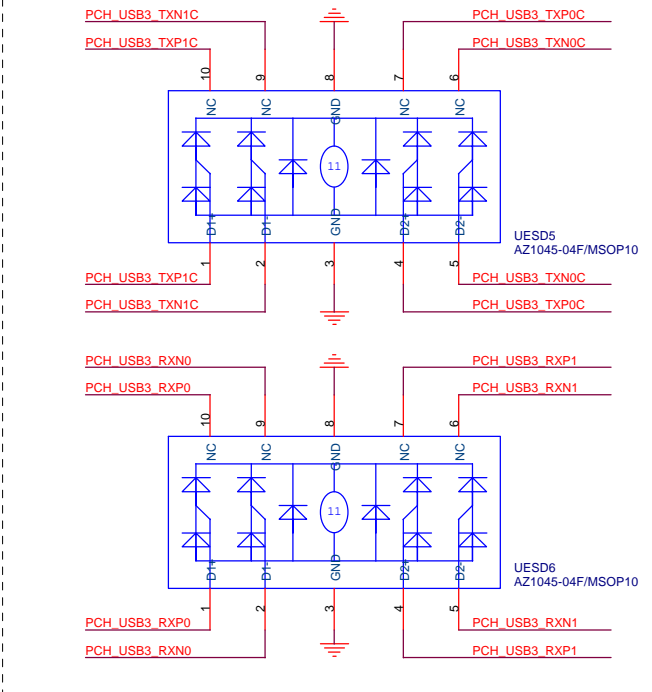
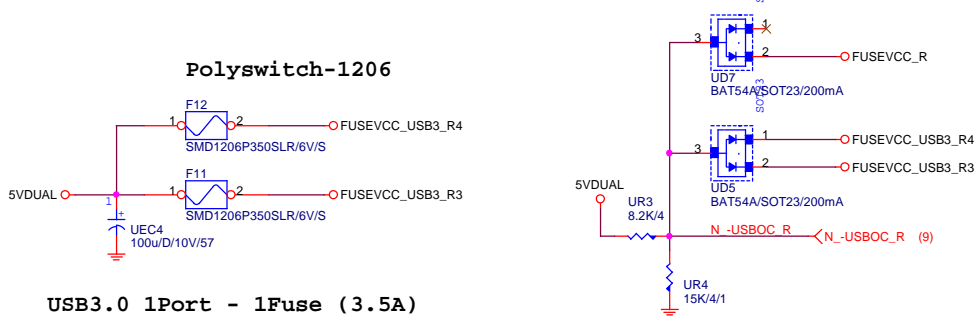
USB3.0 ESD

USB POWER PROTECT

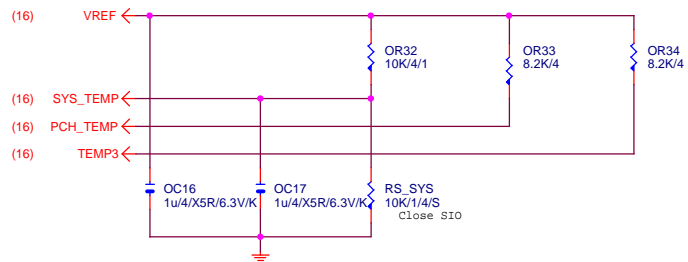


USB30\_20 PWR

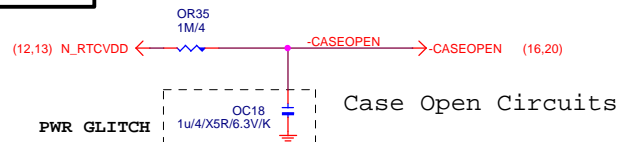
-USBOC\_R



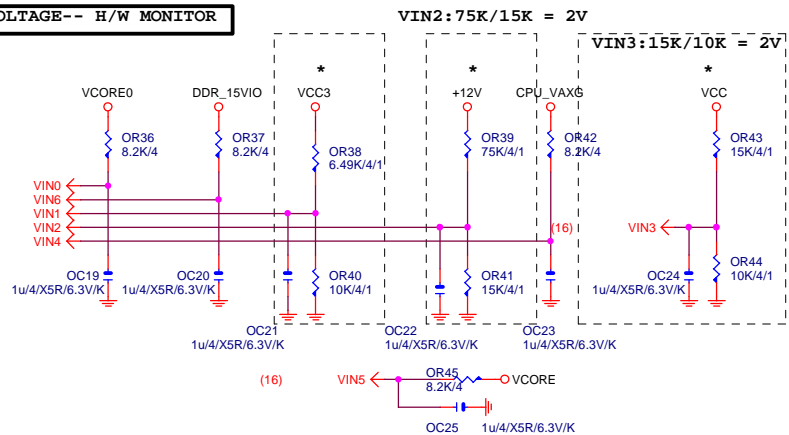
## TEMP H/W MONITOR



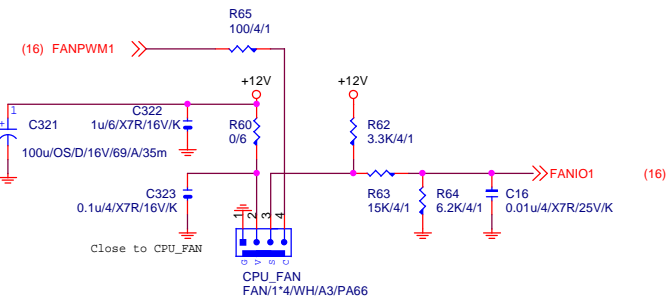
## CASE OPEN



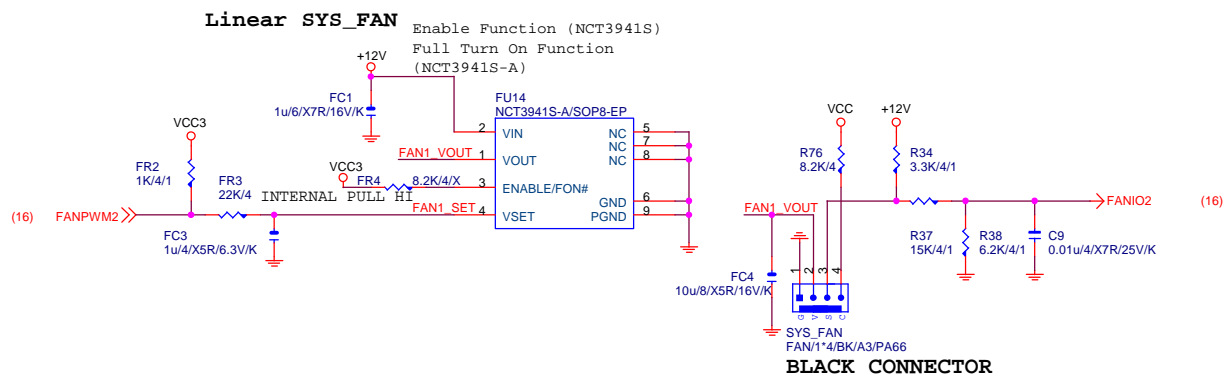
## VOLTAGE-- H/W MONITOR



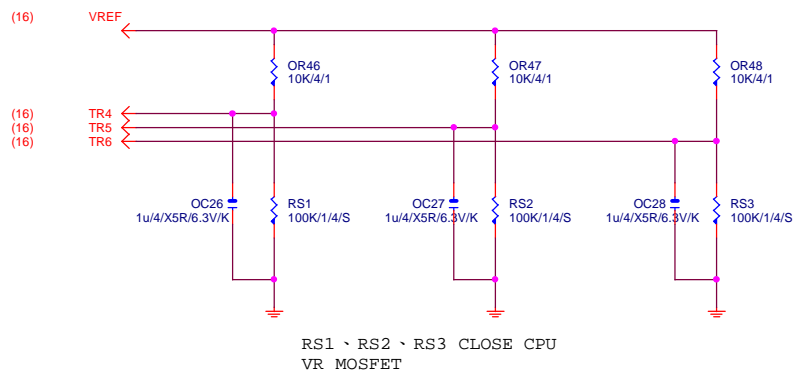
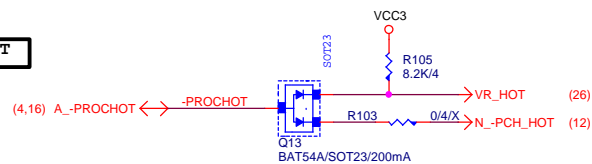
## CPU SMART FAN



## SYS SMART FAN

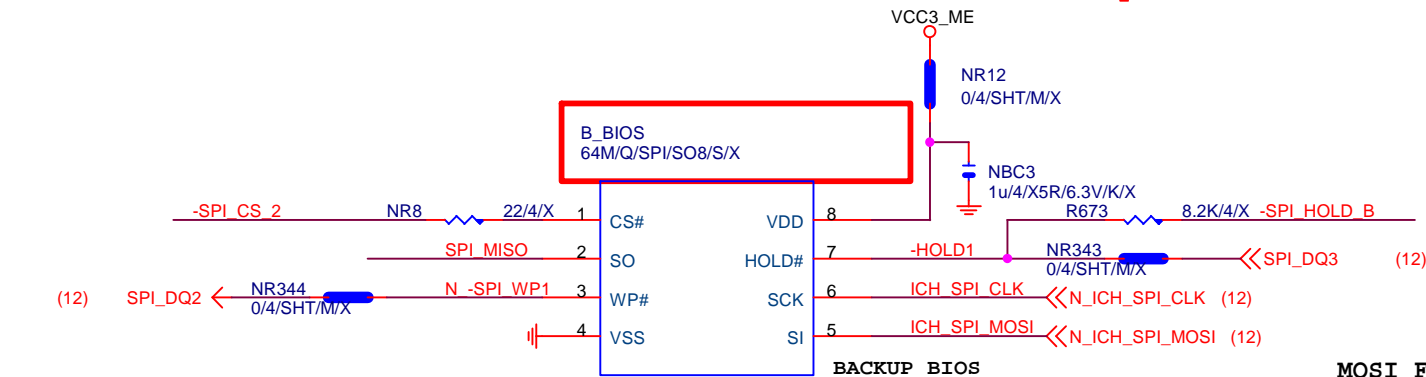
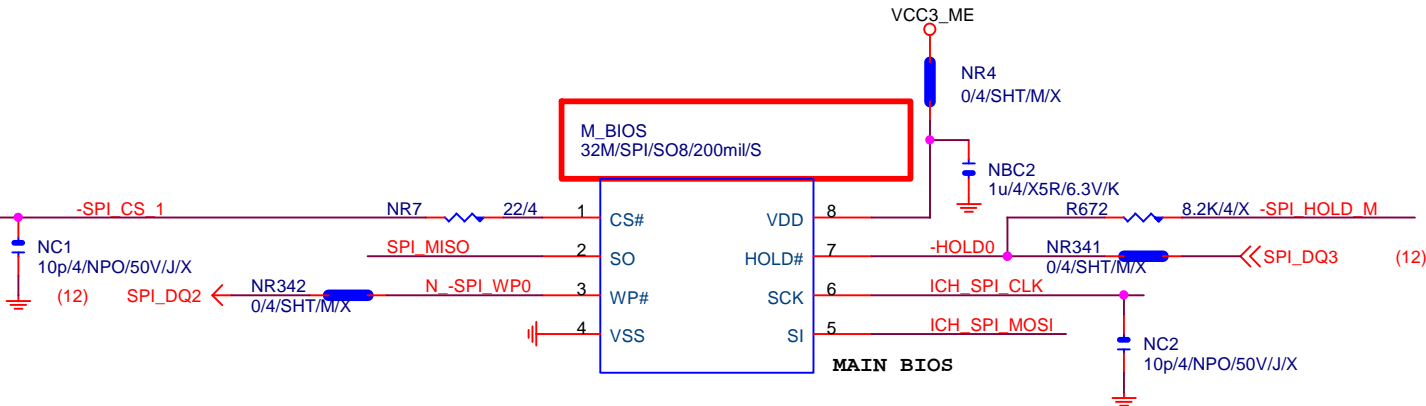


## -PROHOT



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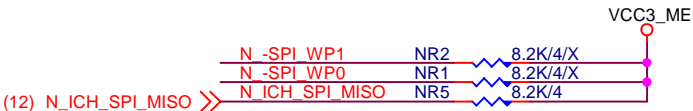
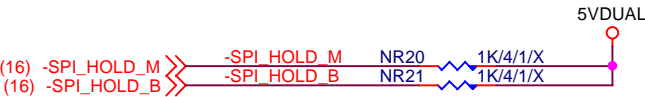
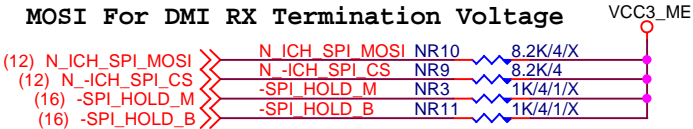
Title			HWM,FAN CTRL,OV	
Size			GA--H81M-H	
Date:			Thursday, October 31, 2013	
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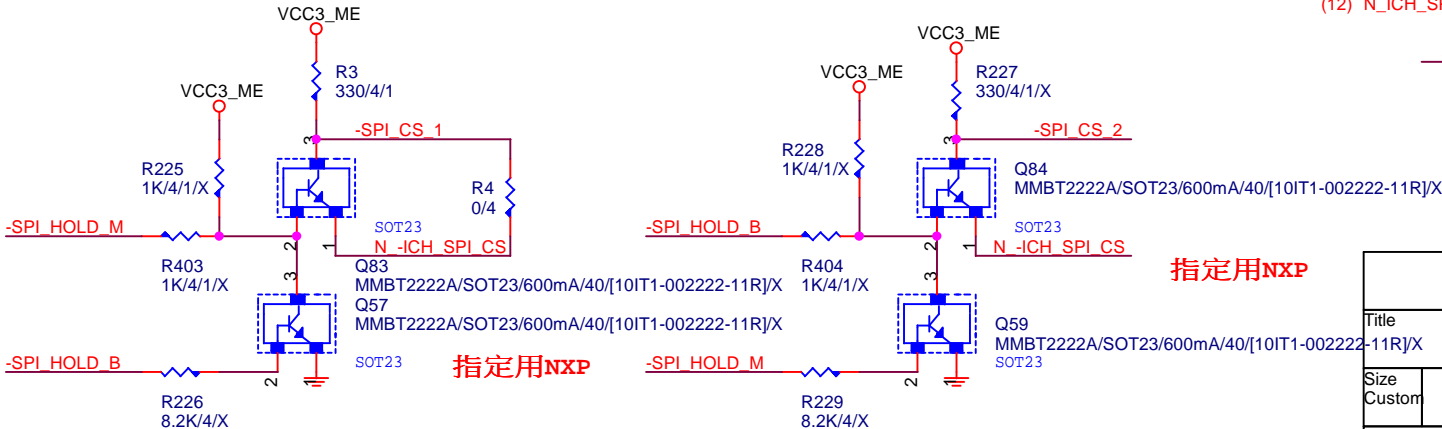
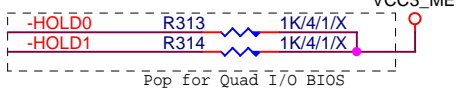
BOOT DEVICE	GNT0	GNT1
LPC	0	0
PCI	0	1
NAND	1	0
SPI	1	1

1 means floating  
0 means PD 1K

MOSI For DMI RX Termination Voltage



CHECK



指定用NXP

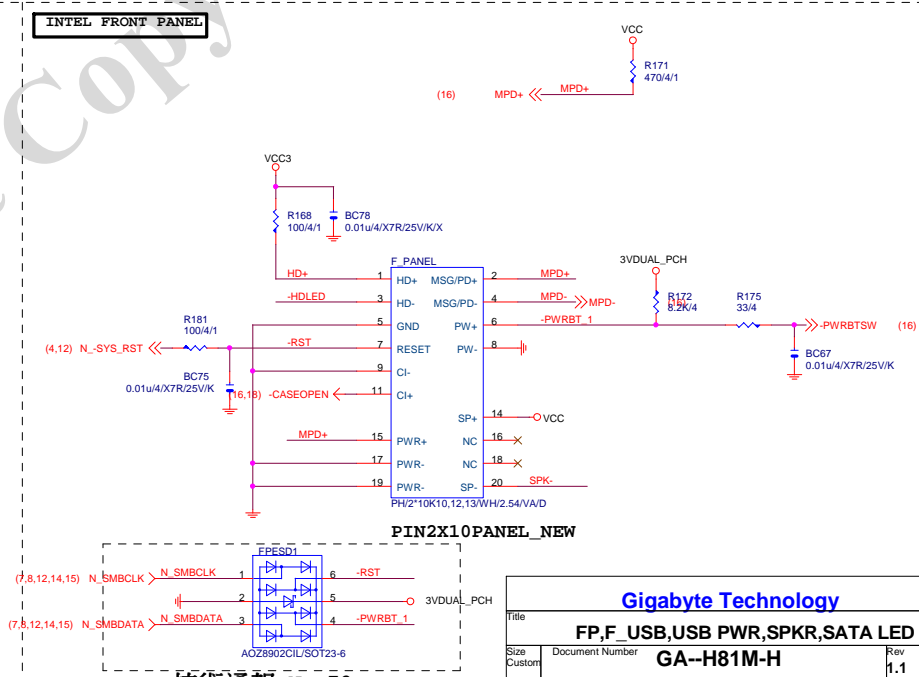
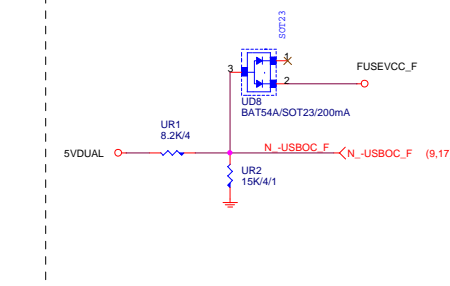
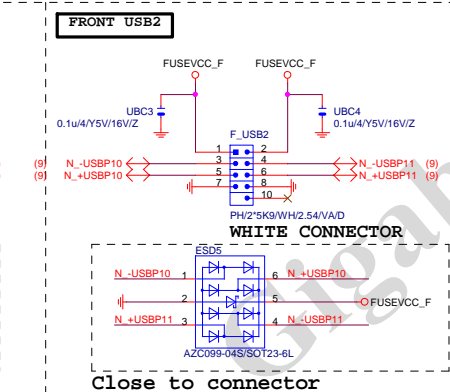
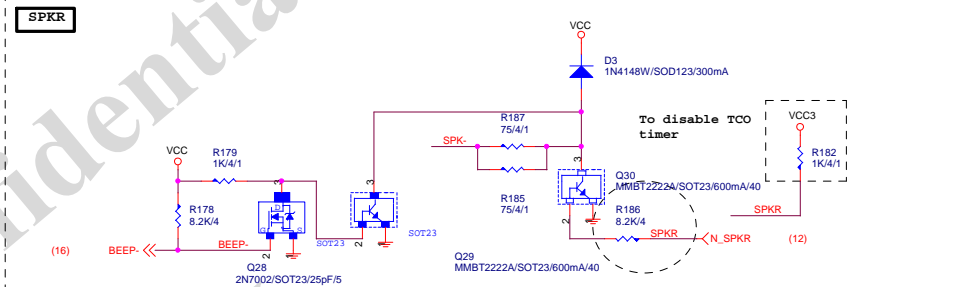
Gigabyte Technology

DUAL BIOS

GA--H81M-H

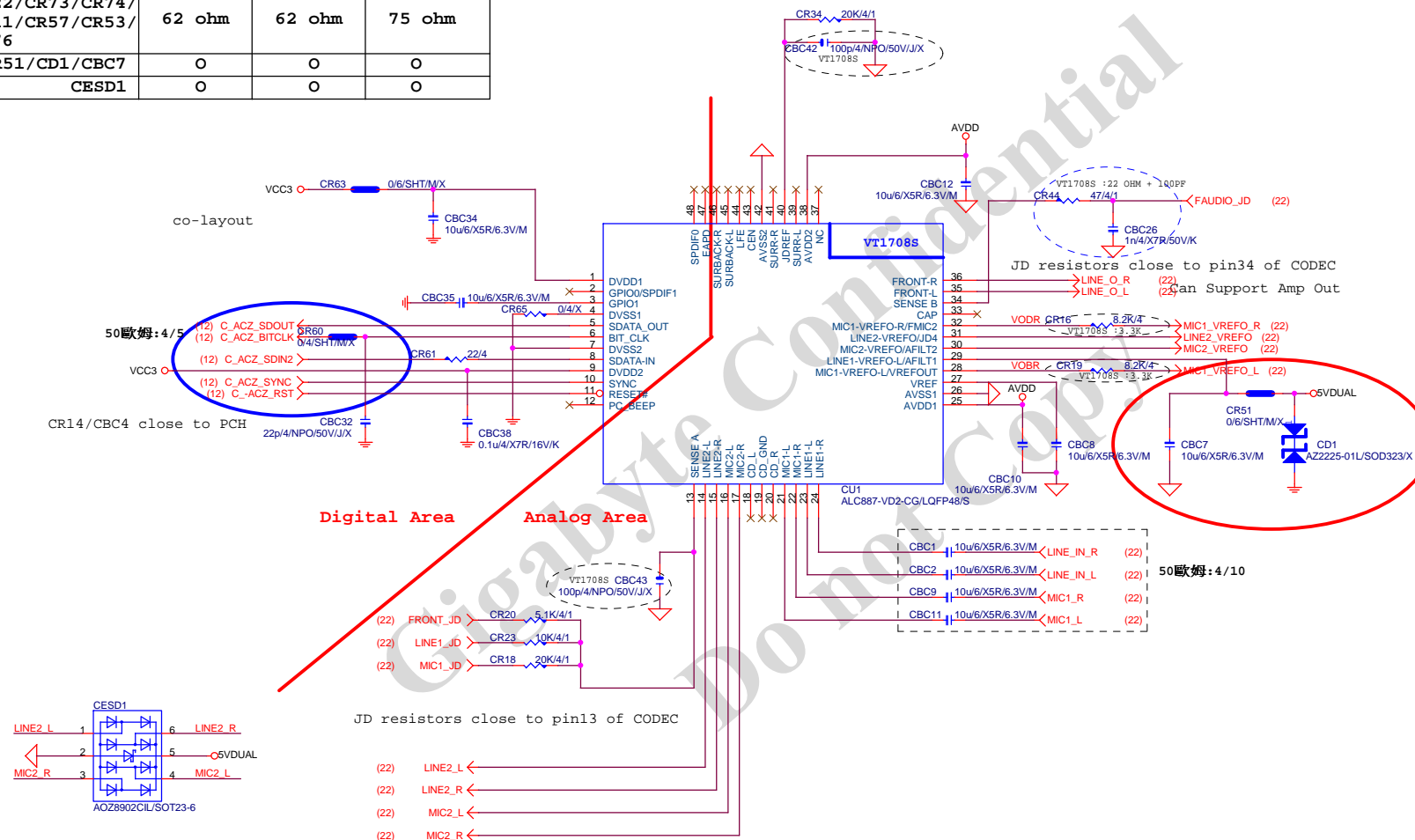
Rev 1.1

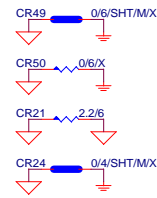
Title	Document Number	Rev
		1.1
Date	Thursday, October 31, 2013	Sheet 19 of 29



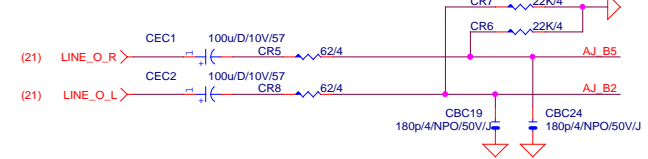
AZALIA CODEC ALC892/ALC887-VD2/VT1708-CE Colay

	ALC892	ALC887-VD2	VT1708S-CE
CR44/CBC26	47ohm+1nF	47ohm+1nF	22ohm+100P
CBC42/CBC43	X	X	100P/4
CR6/CR7/CR58/CR54/ CR67/CR68/CR69/CR70	22K/4	22K/4	10K/4/1
CR5/CR8/CR1/CR14/ CR17/CR22/CR73/CR74/ CR13/CR11/CR57/CR53/ CR75/CR76	62 ohm	62 ohm	75 ohm
CR51/CD1/CBC7	O	O	O
CESD1	O	O	O





## LINE-OUT



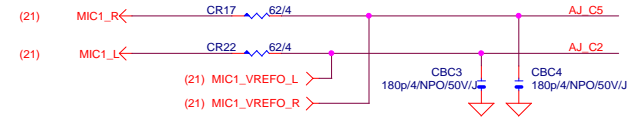
Only reserved for ALC888

## LINE-IN

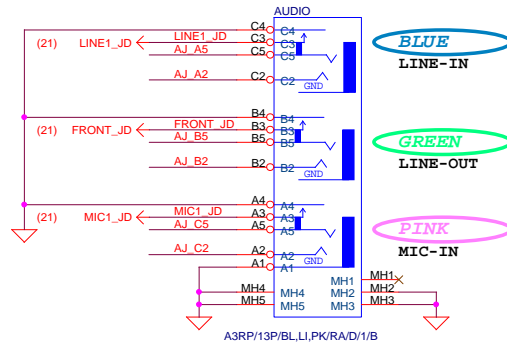
Verify MIC function  
in LINE-in

For 889A/888

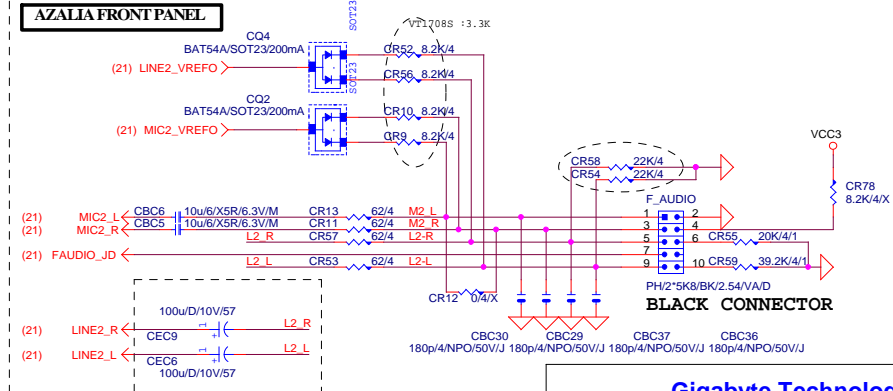
## MIC-IN



## SPDIF\_OUT



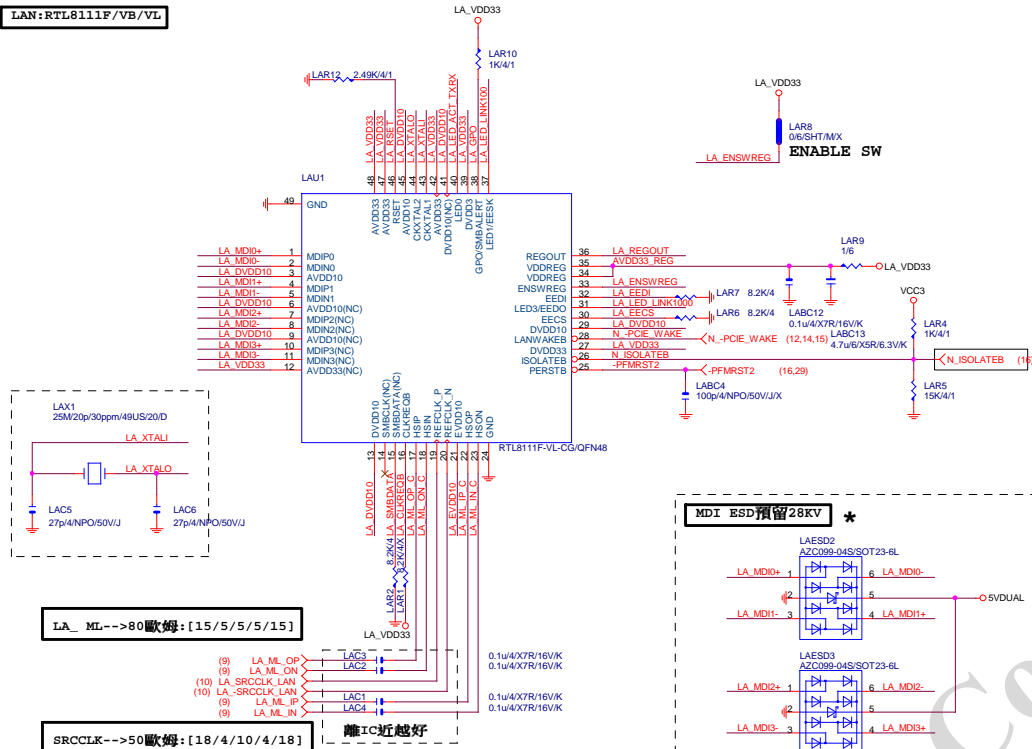
## AZALIA FRONT PANEL



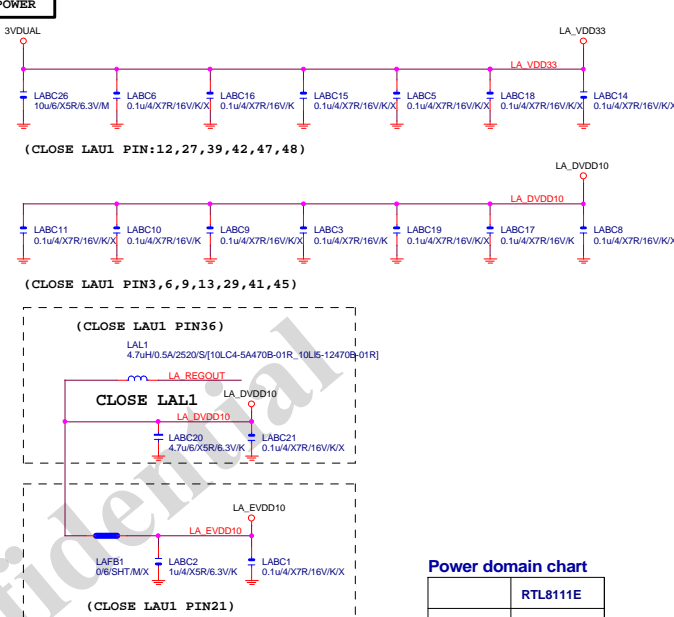
Gigabyte Technology

Title			
AUDIO JACK			
Size	Document Number	Rev	
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LAN:RTL8111F/VB/VL



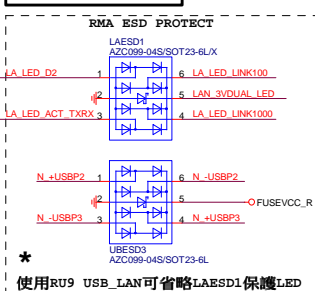
## POWER



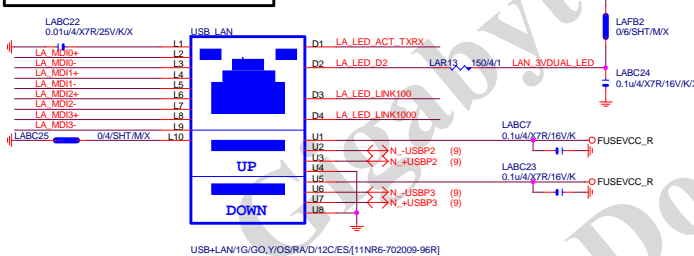
### Power domain chart

	RTL8111E
AVDD33	3.3V
DVDD33	3.3V
VDDREG	3.3V
DVDD10	1.05V

## USB\_LAN CONNECTOR



LA\_MDI-->100歐姆:[20/4/8/4/20]

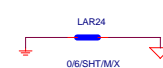


USB X3 POWER



## EMI SHORT PAD

PS:視EMI需求

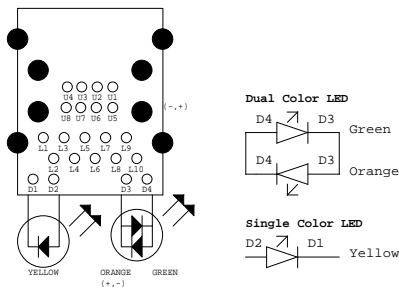


注意:USB PORT(目前:暫代6,7PORT)  
USB-->90歐姆:[15/4.5/7.5/4.5/15]

**BOM NOTICE \***

料號	規格	廠商
11NR6-702009-96R	1G LAN (12core)	UDE(RU9 ESD+)
[LED獨立走線,可省略外加AZC099料件LAESD1]		

1. 9KV ESD BOM:  
USB\_LAN (RU9):11NR6-702009-96R
2. 28KV ESD BOM:  
USB\_LAN (RU9):11NR6-702009-96R  
LAESD2,LAESD3:上件AZC398-04S

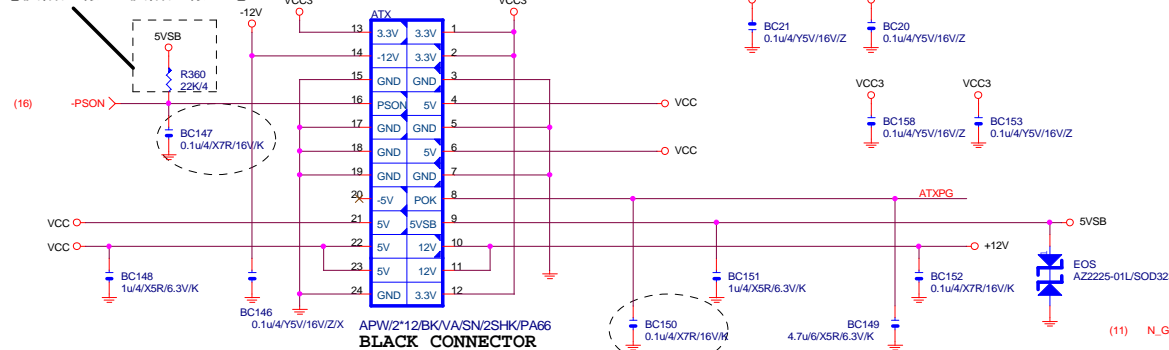






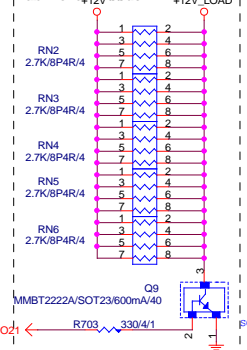
# ATXX24 POWER CONNECTOR

【技術通報R&D技術通報155】



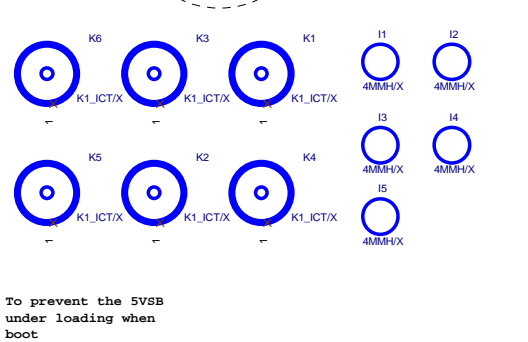
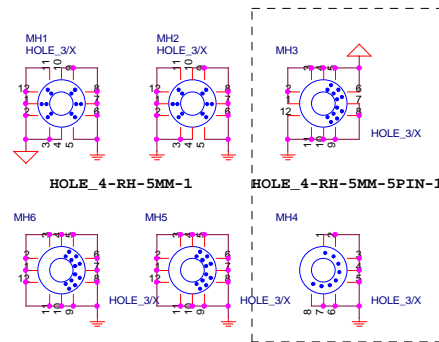
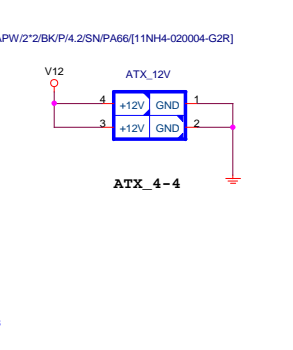
# 【技術通報R&D技術通報153】

To fix 12V light load abnormal issue



# ATXX4 POWER CONNECTOR

APW/Z'2/BK/P/4.2/SN/PA66[11NH4-020004-G2R]

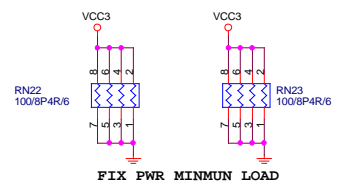
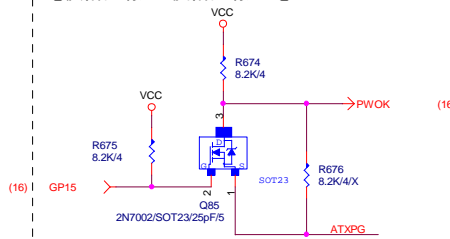


# TPM

To prevent the 5VSB under loading when boot

# PWOK PATCH

【技術通報R&D技術通報154】



FIX PWR MINMUN LOAD

Gigabyte Technology

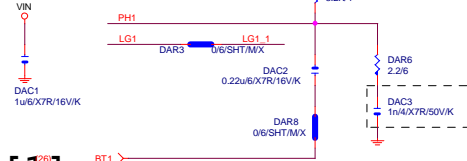
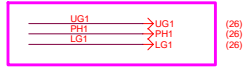
ATX CONNECTOR

GA-H81M-H

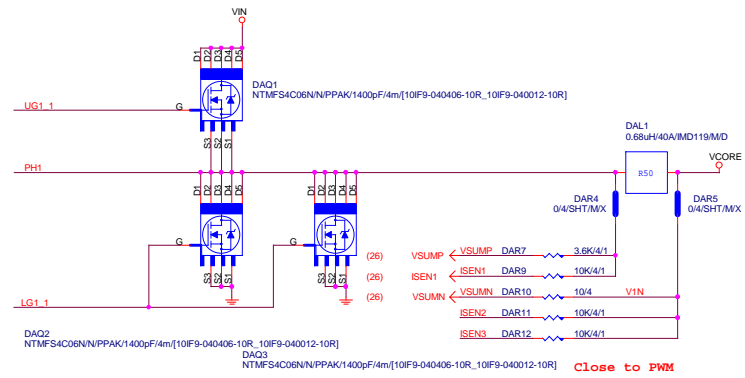
Rev 1.1



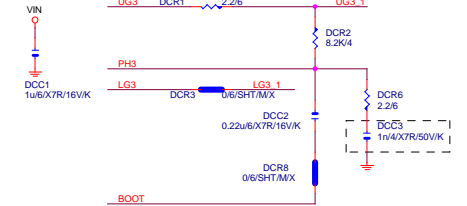
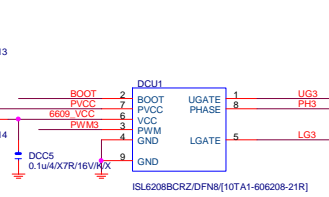
# PHASE 1



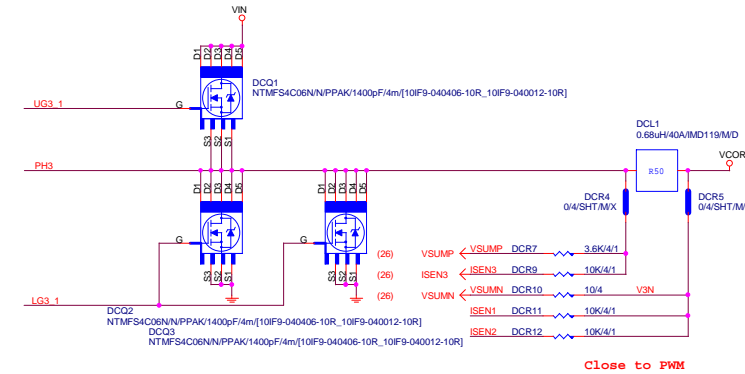
[1]



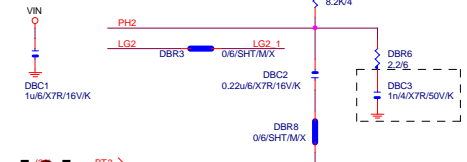
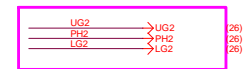
# PHASE 3



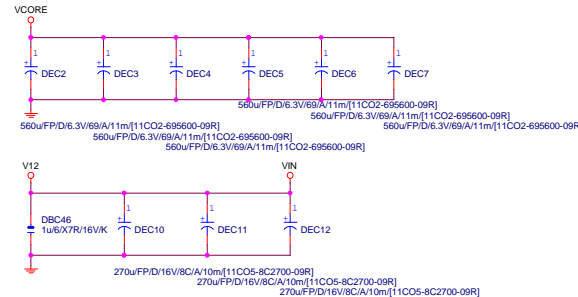
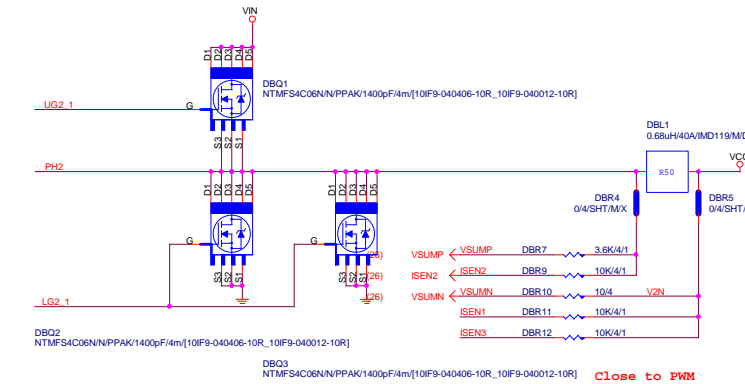
[3]



# PHASE 2




[2]



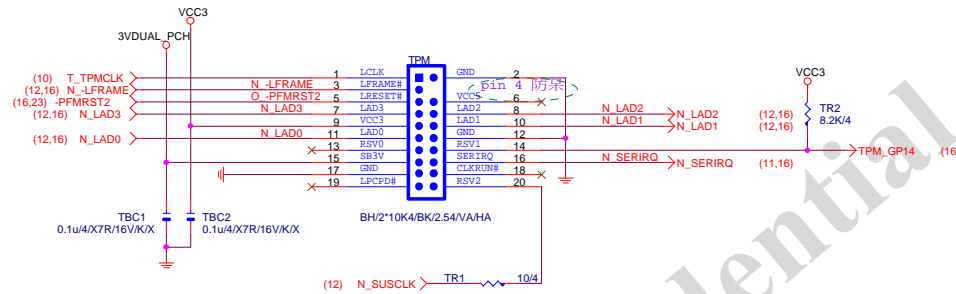
Gigabyte Technology			
Title		CPU CORE VR-2	
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[illegible]

```
Rocset=(Iocp*Lgate,rdson)/Iocset
Rocset=(45A*6.7mOhm)/10uA = 30K
Iocset=10uA
```

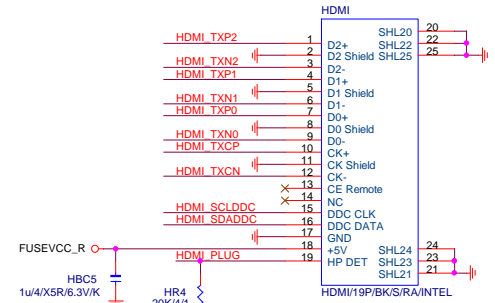
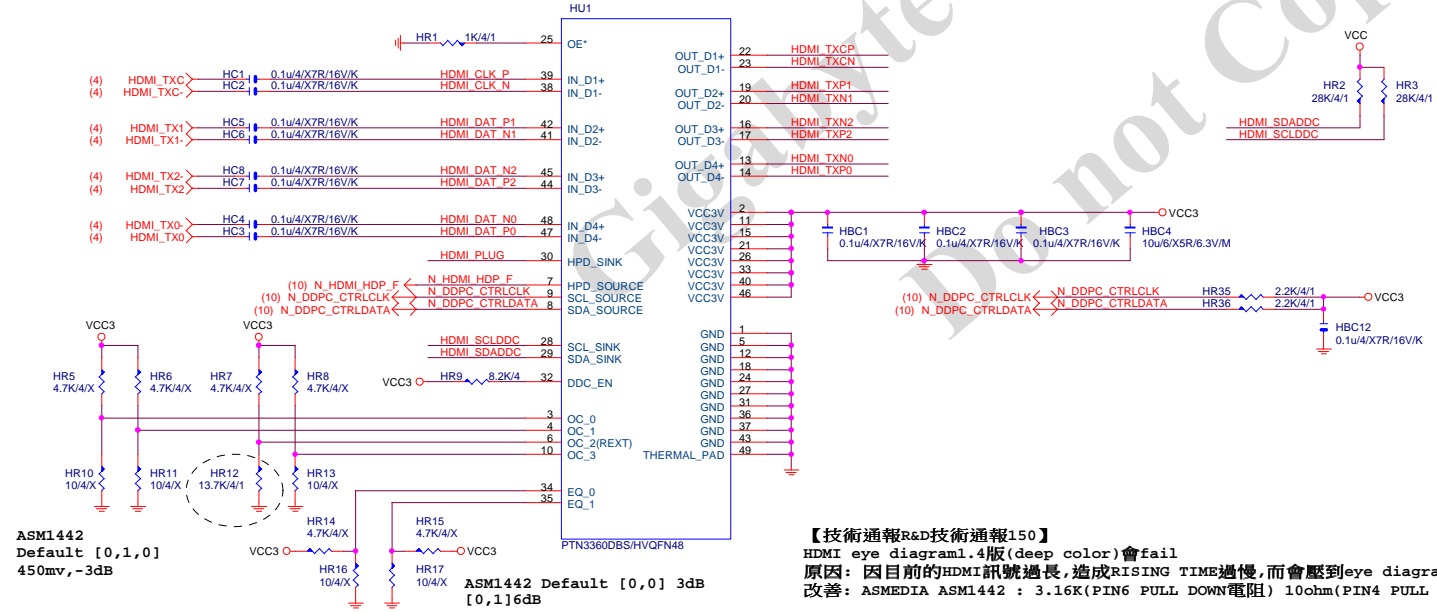
<div style="text-align: center;">  </div>			
Title			
DDR POWER			
Size	Document Number	GA--H81M-H	Rev
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# TPM CONNECT



# HDMI LEVEL SHIFT

HDMI: 20/4/6/4/20  
Impedance=85 +/- 17.5%



【技術通報R&D技術通報150】  
HDMI eye diagram 1.4版(deep color)會fail  
原因: 因目前的HDMI訊號過長,造成RISING TIME過慢,而會壓到eye diagram  
改善: ASMEDIA ASMI442 : 3.16K(PIN6 PULL DOWN電阻) 10ohm(PIN4 PULL DOWN電阻)

Gigabyte Technology		
HDMI		
GA-H81M-H		
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